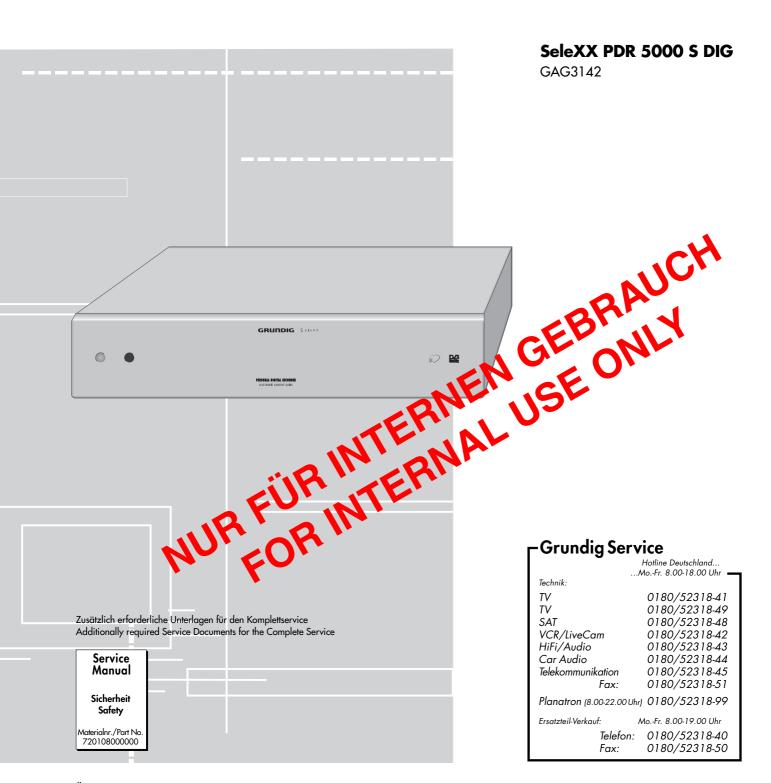


SAT\_\_\_\_\_Service Manual



Änderungen vorbehalten/Subject to alteration E-BS37 0101 http://www.grundig.com Es gelten die Vorschriften und Sicherheitshinweise gemäß dem Service Manual "Sicherheit", Materialnummer 720108000000, sowie zusätzlich die eventuell abweichenden, landesspezifischen Vorschriften!



Seite

The regulations and safety instructions shall be valid as provided by the "Safety" Service Manual, part number 720108000000, as well as the respective national deviations.

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## **Allgemeiner Teil**

### Messgeräte / Messmittel

Regeltrenntrafo Oszilloskop
Digitalmultimeter Frequenzzähler

Beachten Sie bitte das Grundig Messtechnik-Programm, das Sie unter

folgender Adresse erhalten:

## **General Section**

## **Test Equipment / Jigs**

Variable isolating transformer Oscilloscope
Digital multimeter Frequency counter

Please note the Grundig Catalog "Test and Measuring Equipment" obtainable from:

Grundig AG
Geschäftsbereich Instruments
Test- und Mess-Systeme
Würzburger Str. 150, D-90766 Fürth
Tel.: 0911 / 703-4540; Fax: 0911 / 703-4130
eMail: instruments@grundig.com

Internet: http://www.grundig-instruments.de

#### **Service Hinweise**

Vor Öffnen des Gerätes Netzstecker ziehen!

# Bitte beachten Sie vor Beginn der Reparatur die reduzierte Ersatzteilliste.

Die Festplatte ist speziell formatiert. Tauschen Sie bei Bedarf die Festplatte nur durch das orginal GRUNDIG-Ersatzteil.

Nach Austausch der Festplatte **muss** der Testmode gestartet werden, damit das Gerät die Festplatte erkennt.

#### **Service Instructions**

Disconnect the mains plug before opening the set!

# Before starting the repair pay attention of the reduced spare parts list.

The harddisk is especially formatted. If necessary please use only the original GRUNDIG spare part for a change.

After a change of the harddisk the testmode must be started, so that the set can recognize the harddisk.

### **Technische Daten**

TV-Programme	4000 und mehr
Empfangsbereich	
Eingangspegel	
Eingangsimpedanz	75Ω
LNC-Versorgung	
Local-Oszillator GHz	
Schaltsignale	
DiSEqC	
Display 1 Multi-	
OSD	Anzeige, Programmierfunktion
Sprachen OSD	Deutsch, Englisch
Sendersuchlauf Manue	ell oder über Download via Satellit
Videotext	
Modulation	
Symbolrate MS/sec	2 - 30 MCPC
Bildformateinstellung	4:3, 16:9, letterbox conversion
ECG (Electronic Content Guide)	28 Tage im Voraus,
	ıalisiert über Download via Satellit
Festplatte	
. Downloadfunktionen über Satellit .	
	Senderdaten
MPEG-Decodierung	
MPEG 2	Main Level / Main Profile
Video 720	
Audio MPEG 2 Layer	1+2, 16/22,05/24/32/44,1/48 kHz
Prozessor	
CPU	
Speicher 8	Mbyte SDRAM, 20 GB Festplatte
Wiedergabefunktionen	
Bildsuchlauf	
Geschwindigkeit	veränderbar über Fernbedienung
Standbild	
Zeitlupe	
	veränderbar über Fernbedienung
Instant Replay	
Time-Shift	
Aufnahmesteuerung	
	nuelle Aufnahme, Sofortaufnahme
Anschlüsse	
SAT-ZF-Eingang	
SAT-ZF-Durchschleifausgang	
TV, AUX (nur Ausgang)	
S-VHS-Ausgang	
Datenschnittstelle	
Video-Ausgang	
Audio-Ausgang	2x Cinch-Buchse
Digital-Audio-Ausgang	
Infrarot-Fernbedienung	TelePilot 777 DR
Netzteil	
Netzspannung,	
Leistungsaufnahme max. / Standb	
Gewicht ca	
Abmessungen ca. (B x H x T)	36cm x 6,8cm x 26cm

## **Specifications**

Inner the green and consequent	4000 and more
input frequency range	950 - 2150MHz
Input level	
Input impedance	75Ω
LNC power supply	
Local Oscillator GHz	
Switch-over signals	0/22kHz
DiSEqC	
Display 1 Multi-Colour-LEI	
OSD	
OSD languages	
Station search manually or via	download from satellite
Teletext	
Modulation	
Symbol rate MS/sec	
Picture formats 4:3, 16	
ECG (Electronic Content Guide)	
> 35 stations, daily updated via	download from catallita
So stations, daily updated via Harddisk	
Download function via satellite	
Download function via satellite	
MDEC Deceding	station data
MPEG Decoding MPEG 2	Main lavel / Main mustila
Video	0/6 lines x 25 trame/sec
Audio MPEG 2 Layer 1+2, 16/2	2,05/24/32/44,1/48 kHz
Processor	D:: 00
CPU	
Memory 8 Mbyte S	DRAM, 20 GB harddisk
Playback funktions	
Picture search	
speed var	riable via remote control
Freece frame	yes
Freece frame	yes
Freece frame	yesyes yes, riable via remote control
Freece frame	yes,yes yes, riable via remote control
Freece frame	yes,yes, riable via remote control7sec20 – 60min
Freece frame	yes,
Freece frame	yes,yes, riable via remote control7sec20 – 60min
Freece frame Slow motion speed var Instant replay Time-shift Record control manual reco	yes, yes, yes, iable via remote control 7sec 20 – 60min via ECG, ord, one touch recording
Freece frame Slow motion speed var Instant replay Time-shift Record control manual record Connections  SAT IF input 1 F-con	yes, yes, yes, yes, riable via remote control 7sec 20 – 60min via ECG, ord, one touch recording nector socket (75 Ohm)
Freece frame Slow motion speed var Instant replay Time-shift Record control manual reco	yes, yes, yes, yes, riable via remote control 7sec 20 – 60min via ECG, ord, one touch recording nector socket (75 Ohm)
Freece frame Slow motion speed var Instant replay Time-shift Record control manual record Connections  SAT IF input 1 F-con	yesyes, yes, yes, yes, yes, iable via remote control 7sec
Freece frame	yesyes, iable via remote control 7sec
Freece frame	yesyes, iable via remote control
Freece frame Slow motion speed var Instant replay manual record control manual record Connections  SAT IF input 1 F-con SAT IF bridging output TV, AUX (only output) S-VHS output	yesyes, iable via remote control 7sec
Freece frame Slow motion speed var Instant replay Time-shift Record control manual reco  Connections SAT IF input TV, AUX (only output) S-VHS output Data interface Video output	yes yes, yes, yes, yes, yes, iable via remote control 7-sec 20 – 60min via ECG, ord, one touch recording nector socket (75 Ohm) inactive 2x EURO-AV Hosiden RS 232
Freece frame	yes yes, yes, yes, yes, yes, iable via remote control 7-sec 20 – 60min via ECG, ord, one touch recording nector socket (75 Ohm) inactive 2x EURO-AV Hosiden RS 232 1x cinch socket
Freece frame	yes yes, yes, yes, yes, yes, yes, iable via remote control 7sec 20 – 60min via ECG, ord, one touch recording nector socket (75 Ohm) nactive 2x EURO-AV Hosiden RS 232 1x cinch socket 2x cinch socket optical
Freece frame Slow motion speed var Instant replay Time-shift Record control manual reco  Connections SAT IF input TV, AUX (only output) S-VHS output Data interface Video output Audio output  Slow motion Speed var Audio output manual reco manual r	yes yes, yes, yes, yes, yes, yes, iable via remote control 7sec 20 – 60min via ECG, ord, one touch recording nector socket (75 Ohm) nactive 2x EURO-AV Hosiden RS 232 1x cinch socket 2x cinch socket optical
Freece frame	yes yes, yes, yes, yes, yes, yes, yes, y
Freece frame	yes yes, yes, yes, yes, yes, yes, yes, y
Freece frame	yes yes, yes, yes, yes, yes, yes, yes, y
Freece frame	yes yes, yes, yes, yes, yes, yes, yes, iable via remote control 7sec 20 – 60min via ECG, ord, one touch recording nector socket (75 Ohm) inactive 2x EURO-AV Hosiden RS 232 1x cinch socket 2x cinch socket optical TelePilot 777 DR 47–63 Hz, 220 – 240V 47–63 Hz, 2

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Allgemeiner Teil / General Section SeleXX PDR 5000 S DIG

#### **Ausbauhinweise**

#### 1. Frontblende

- 3 Schrauben (A) (Fig.1) herausschrauben.
- Frontblende abziehen.

#### 2. Gehäuseoberteil

- Frontblend abnehmen (Punkt 1).
- 6 Schrauben (B) (Fig.1,2) herausschrauben.
- Gehäuseoberteil abnehmen.

#### 3. Netzteil

- Gehäuseoberteil abnehmen (Punkt 2).
- 4 Schrauben © (Fig.1) herausschrauben.
- Steckverbindungen bei Bedarf abziehen.

#### 4. Festplatte

- Gehäuseoberteil abnehmen (Punkt 2).
- 4 Schrauben D (Fig.4) herausschrauben.
- Steckverbindungen bei Bedarf abziehen.

#### 5. Hauptplatte

- Gehäuseoberteil abnehmen (Punkt 2).
- 3 Schrauben (E) (Fig.2,3) herausschrauben.
- Festplattenhalter herausnehmen.
- Abdeckung des Digitalausgangs (F) (Fig.2) abziehen.
- 6 Schrauben (G) (Fig.5) herausschrauben.
- Schraube (H) (Fig.2) herausschrauben.
- 2 Sub-D-Stecker-Schrauben () (Fig.2) herausschrauben.
- 2 Sechskantmuttern (K) (Fig.2) herausschrauben.

## **Disassembly Instructions**

#### 1. Front Cover

- Undo 3 screws (A) (Fig.1).
- Pull the front cover to the front.

#### 2. Cabinet Top

- Remove the front cover (para 1).
- Undo 6 screws B (Fig.1,2).
- Remove the cabinet top.

#### 3. Mains Unit

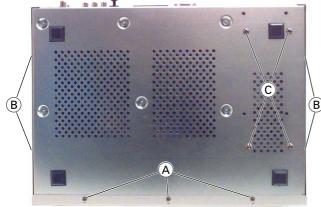
- Remove the cabinet top (para 2).
- Undo 4 screws C (Fig.1).
- If necessary undo the connectors.

#### 4. Hard Disk Drive

- Remove the cabinet top (para 2).
- Undo 4 screws D (Fig.4).
- If necessary undo the connectors.

#### 5. Main Board

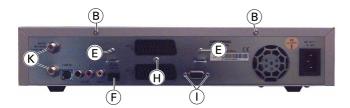
- Remove the cabinet top (para 2).
- Undo 3 screws (E) (Fig.2,3).
- Remove the hard disk drive holder.
- Pull up the cover of the digital output (F) (Fig.2).
- Undo 6 screws (G) (Fig.5).
- Undo screw (H) (Fig.2).
- Undo 2 Sub-D connector screws (1) (Fig.2).
- Undo 2 hexagonal nuts (K) (Fig.2).



B

Fig. 4

Fig. 1





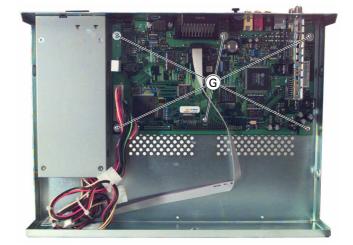


Fig. 5

1 - 4

Fig. 2

SeleXX PDR 5000 S DIG Allgemeiner Teil / General Section

#### **Testmode**

#### 1. SeleXX Endtest V1.2

Für den Endtest der MediaTV ist das Gerät vollständig, d.h. mit Verbindung zur seriellen Schnittstelle, dem SAT-Anschluss, sowie der Netzspannung, etc. aufzubauen. Mit dem PC können die Statusmeldungen und Informationen während der Ausführung der Tests erfasst werden. Um diese Daten lesen zu können, ist auf dem PC eine entsprechende Software, z.B. Hyperterminal (Windows 95/98), zu verwenden.

Einstellungen für die Datenübertragung:

Bits/s: 19200
Datenbits: 8
Parität: keine
Stopbits: 1
Protokoll: kein

Bestandteil des Endtestes ist ein Komponententest der mediaTV und ein Dauertest bei gestartetem Betriebssystem. Beide Varianten werden in den folgenden Abschnitten beschrieben.

#### 1.1 Der Komponententest

Nach Einschalten der Spannungsversorgung kann der Komponententest auf zwei verschiedene Arten durchgeführt werden. Zum einen per Fernbedienung (End-User) und zum anderen per V24-Kommando (Produktion).

#### 1.1.1 Testdurchführung per Fernbedienung

Der Komponententest per Fernbedienung wird gestartet, indem beim Bootvorgang die Taste 9 der Fernbedienung gedrückt gehalten wird. Der Start der Testserie wird durch ein kurzzeitiges Blinken der LED angezeigt und läuft, bis auf den Test des IR-Empfängers und der Fernbedienung, automatisch ab. Beim Komponententest werden die einzelnen Testfunktionen (siehe 1.1.3) einmalig und sequentiell abgearbeitet.

#### 1.1.2 Testdurchführung mit einem PC

Wird der Komponententest mit einem Testautomaten durchgeführt, so können die einzelnen Tests (siehe 1.1.3) in beliebiger Reihenfolge und beliebig oft ausgeführt werden. Auf diese Weise können Tests übersprungen werden, um die Ausführungszeit aller Tests zu minimieren, oder aber Tests mehrmalig ausgeführt werden, falls sich Probleme bei der Testdurchführung ergeben haben.

Der Endtest kann im Kommandomodus betrieben werden, wenn an der V24-Schnittstelle die Signale CTS (Pin 7) und RTS (Pin 8) verbunden sind. Wurde die Brücke beim Booten richtig erkannt, so wird das Kommando-Prompt "#R:" angezeigt. An dieser Stelle kann mit dem Kommando "START" in den kommandogesteuerten Komponententest gewechselt werden, oder mit "YP§){OB [param]" (param=Senderparameter z.B. ARD) der Dauertest, bei gestartetem Betriebssystem, ausgeführt werden (siehe Tabelle 1). Mit "QUIT" lässt sich der Bootvorgang normal fortsetzen.

Kommando im Brückenbetrieb	Aktion
START	Start des V24-Komponententests
YP§){OB [param]	Starten des Betriebssystemtests mit <i>param</i> = Senderparameter
	z.B. ARD
QUIT	Fortsetzung des Bootvorgangs

Tabelle 1: Kommandos nach V24-Brückenerkennung

Wurde der kommandogesteuerte Komponententest ausgewählt, so können verschiedene Testfunktionen (siehe Tabelle 2) ausgeführt werden, sobald das Prompt "#R:" erscheint. Um die erzielten Ergebnisse mit einem Testautomaten auswerten zu können, sind die Zeilen mit dem resultierenden Ergebnis mit dem Schlüsselwort "#RSLT:" gekennzeichnet. Wurde ein Test mit einem Fehler beendet, so wird die Fehlerausgabe mit dem Schlüsselwort "#F:" eingeleitet. Werden durch die Testfunktionen Versionsangaben ausgegeben, so befindet sich am Anfang der Zeile das Kürzel "#V:". Verlassen wird der Komponententest mit dem Kommando "QUIT".

Kommando	Testfunktion
AVIASDRAM	AVIA Speichertest
AVIAHOST	AVIA Registertest
AVIATRSP	AVIA Testbildschirm und MPEG-Film
RTCREG	RTC Registertest
RTCALARM	RTC Alarm A&B
RTCTIMESET	RTC Time Set&Get
TUNER	TUNER Test mit fester Frequenz
	· ·

#### **Testmode**

#### 1. SeleXX Final Test V1.2

For the final test of the MediaTV, the set is to be set up completely, that is, with the connection to the serial interface, the SAT connection, the mains voltage supply, etc. The PC enables to record the status messages and information during the execution of the tests. In order to be able to read this data, an appropriate software, e.g. Hyperterminal (Windows 95/98), must be installed on the PC.

Settings for the data transmission:

Bits/s: 19200
Data bits: 8
Parity: none
Stop bits: 1
Protocol: none

Part of the final test is a component test of the MediaTV and a continuous test with the operating system running. Both variants are described in the following paragraphs.

#### 1.1 The Component Test

After switching on the power supply, it is possible to carry out the component test in two different ways. One test method is carried out with the remote control (end user), while the other test method is carried out via the V24 command (production).

#### 1.1.1 Test with the Remote control

The component test with the remote control is started while holding down the button 9 on the remote control during booting. Starting of the test series is indicated by a brief flashing of the LED and runs automatically with the exception of the test of the IR receiver and of the remote control. During the component test, the individual test functions (see 1.1.3) are carried out once in a sequential order.

#### 1.1.2 Test Execution with an PC

If the component test is carried out with an automatic testing equipment, the individual tests (see 1.1.3) can be carried out in random order and as often as desired. In this way, it is possible to skip certain tests in order to minimize the execution time of all tests, or to carry out certain tests several times if there should be problems during the execution of the tests.

The final test can be carried out in command mode if the CTS (Pin 7) and RTS (PIN 8) signals are connected to the V24 interface. If the bridge has correctly been recognized during booting, the command prompt "#R:" is indicated. At this point, it is possible to go to the command-controlled component test by entering the "START" command, or it is possible to carry out the continous test with running operating system by means of "YP§){OB [param]" (param=station parameter, e.g. ARD; see Table 1). Using the "QUIT" command, it is possible to continue booting in a normal way.

Command in bridge mode	Action
START	Start of the V24 component test
YP§){OB [param]	Start of the operating system test with <i>param</i> = station parameter, e.g. ARD
QUIT	Booting continuation

Table 1: Commands after bridge recognition.

If the command-controlled component test has been selected, various command-controlled component tests can be carried out (see Table 2) as soon as the "#R:" prompt appears. In order to be able to evaluate the obtained results with the help of an automatic testing equipment, the rows showing the results are marked with the "#RSLT:" code. If a test has been concluded with an error, the error output is startet with the "#F:" code. If version indications are output through the test function, the code "#V:" is to be found at the beginning of the row. Enter the "QUIT" command if you wish to exit the component test.

Command	Test function
AVIASDRAM	AVIA memory test
AVIAHOST	AVIA register test
AVIATRSP	AVIA test screen and MPEG film
RTCREG	RTC register test
RTCALARM	RTC alarm A&B
RTCTIMESET	RTC time Set&Get
TUNER	TUNER test with fixed frequency
XILINX	XILINX register and puffer test
TUNEXILINX	TUNER and XILINX test
IDEDATA	Indication of IDE data

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Allgemeiner Teil / General Section SeleXX PDR 5000 S DIG

**XILINX TUNEXILINX IDEDATA IDEREG** REMCTRL **GETCONF** SETPRNO [param]

**TUNER und XILINX-Test** Anzeige IDE-Daten IDE Registertest Fernbedienungstest Anzeige der Versionsangaben

XILINX Register- und Puffertest

Schreiben einer Integer Projekt-Num-

mer

**GETPRNO** Lesen der Projekt-Nummer Ende des V24-Komponententests QUIT

Tabelle 2: Kommandos zur Ausführung von Testfunktionen

#### 1.1.3 Inhalte des Komponententests

#### **OSD** für Testsoftware

Hiermit wird dem Benutzer signalisiert, dass der Komponententest durchgeführt wird. Der Benutzer erhält über das OSD die Information, welche Testfunktion aktuell ausgeführt wird und in welchem Zustand die Funktionen beendet wurden.

#### Hauptspeicher

In diesem Test werden die Register des Hauptspeichers mittels Lese- und Schreiboperationen getestet. Um das System bei Bedienung der Fernbedienung während dieses Test nicht zu beeinflussen, wird dieser Test sowohl bei der Testdurchführung mit der Fernbedienung als auch mit einem Testautomaten immer als erster Test durchgeführt. Da der Test nur einmalig durchgeführt wird, existiert auch kein Kommando für den Testautomaten (siehe Tabelle 2).

#### • AVIA (MPEG-Decoder)

Vor Ausführung dieses Tests wird zunächst die Hardware und der AVIA-Chip neu Initialisiert. Im Anschluss daran wird ein Speichertest (SDRAM) des AVIA-Chips durchgeführt. Danach erfolgt ein Lese- und Schreibtest der Register des Hostinterfaces, sowie eine Überprüfung des Transportinterfaces. Der Test des Transportinterfaces erfolgt durch Schreibvorgänge, die wiederum durch Leseoperationen über das Hostinterface, aus dem Speicher des AVIA-Speichers, überprüft werden. Bei dem Test des Transportinterfaces wird zunächst für ca. 10 Sekunden ein Farbbalken-Testbild angezeigt. Danach wird eine Filmsequenz geladen und am Fernseher angezeigt. Wurde der Komponententest per Fernbedienung gestartet, so wird die Filmsequenz für 30 Sekunden angezeigt. Bei einer Testdurchführung mit einem Testautomaten (siehe 1.1.2) wird die Filmsequenz solange angezeigt, bis vom Testautomaten ein CR-Signal über die serielle Schnittstelle gesendet wurde.

#### RTC

In einem ersten Test der Real-Time-Clock werden Lese- und Schreiboperationen auf die RTC-Register per MBUS durchgeführt. Dabei werden die verschiedenen Zeit-, Datums- und Alarmregister auf Bitfehler getestet. Ein weiterer Test überprüft die Funktionalität der Uhrzeit, indem die Uhr zunächst auf eine definierte Zeit gesetzt wird. Nach einer zeitlichen Differenz, von z.B. einer Sekunde, erfolgt ein erneuter Zugriff auf die Uhr, wobei die fortgeschrittene Uhrzeit ausgelesen, und überprüft wird, ob zwischen den beiden lesenden Zugriffen die zeitliche Differenz ermittelt werden kann. Ein abschlie-Bender Test für die RTC sieht eine Überprüfung der Interrupts (Alarm-A und Alarm-B) vor. Dabei werden insbesondere die Interrupts zum Coldfire getestet.

Bei der an der IDE-Schnittstelle angeschlossenen Festplatte werden in diesem Test die Adress- und Datenleitungen überprüft.

#### TV-TUNER

Zunächst wird mittels Lese- und Schreiboperationen ein Registertest durchgeführt. Im Anschluss daran folgt ein TUNE-Funktionstest. Dieser Test wird sowohl mit und ohne DISEQC ausgeführt. Bei diesem Test wird geprüft, ob ein Sender (ARD – Das Erste) mit einer festen Frequenz innerhalb einer vorgegebenen Anzahl an Fehlversuchen (z.B. 5) eingestellt werden kann.

#### XILINX

In einem ersten Test wird ein Download des UCODE vom Baustein vorgenommen und dieser Code auf Vollständigkeit überprüft. Weitere Tests überprüfen die PID- und die Adress-Puffer.

**IDEREG** REMCTRL **GETCONF** SETPRNO [param] **GETPRNO** QUIT

Remote control test Version indication Writing of an integer project number Reading of the project number End of the V24 component test

IDE register test

Table 2: Commands for carrying out the test functions.

#### 1.1.3 Contents of the Component Tests

#### · OSD for test software

This signals the user that the component test is running. The OSD provides the user with information about what test function is currently being carried out and in which state the functions have been ended.

#### Main Memory

With this test, the registers of the main memory are tested by means of read and write operations. To prevent affecting the system through operation of the remote control during the test, this test is always carried out as first test both when testing with the remote control and with an automatic testing equipment. As this test is carried out only once, there exists no command for the automatic testing equipment (see Table 2).

#### • AVIA (MPEG Decoder)

Before carrying out this test, the hardware and the AVIA chip are at first newly initialized. Following this a memory test (SDRAM) of the AVIA chip is carried out. Following this a read and write test of the registers of the host interface as well as a check of the transport interface is performed. The test of the transport interface is effected by performing write operations which in turn are checked by read operations via the host interface from the memory of the AVIA memory. When testing the transport interface, first a colour bar test pattern is displayed for about 10 seconds. Following this a film sequence is loaded and displayed on the TV set. If the component test has been started via the remote control, the film sequence is displayed for about 30 seconds. When the test is carried out via an automatic testing equipment (see 1.1.2), the film sequence is displayed until a CR signal has been transmitted from the automatic testing equipment via the serial interface.

#### • RTC

In a first test of the real time clock, read and write operations on the RTC registers are effected via the MBUS. In doing this, the various time, date and alarm registers are tested for bit errors. A further test checks the functionality of the clock time by setting the clock to a specific time. After a time period of, for example 1 second, the clock is accessed again and the advanced time is read out and checked to see whether a time shift can be noticed between the two readings. A final test of the RTC serves for checking the interrupt (alarm A and alarm B). With this test the interrupts to the coldfire are especially tested.

#### IDE

With this test, the address and data lines of the hard disk connected to the IDF interface are checked.

#### TV TUNER

First a register test is carried out by means of read and write operations. Following this a TUNE function test is carried out. This test is carried out both with and without DISEQC. This test checks wether a station (ARD - Das Erste) with a fixed frequency can be tuned to within a specified number of erroneous attempts (e.g. 5).

#### XILINX

In a first test a download of the UCODE from the module is carried out and the code is checked for completeness, Further tests check the PID and address buffers.

#### TV TUNER and XILINX

This function test comprises a TUNER test using the XILINX. In doing this, first a TUNE function test for a specified station (ARD -Das Erste) is carried out. If the station could be found, the received messages of the station are checked for their validity, that is the

#### • TV-TUNER und XILINX

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Der Funktionstest beinhaltet einen Test des TUNERS unter Verwendung des XILINX. Dabei wird zunächst ein TUNE-Funktionstest für einen festen Sender (ARD – Das Erste) durchgeführt. Konnte der Sender gefunden werden, so werden die gelesenen Nachrichten des Senders auf ihre Gültigkeit, d.h. Reihenfolge der Nachrichtenpakete und Einhaltung der Paketlänge, überprüft.

## • IR-Empfänger und Fernbedienung

Durch Interaktion mit dem Benutzer können in diesem Test, die Funktionen der Fernbedienung und des Infrarotempfängers getestet werden. Dabei werden durch das Testprogramm nacheinander die einzelnen Tasten der Fernbedienung vorgegeben, die per Fernbedienung zu betätigen sind.

#### · Anzeige von Versionsdaten

Wird der Endtest im Kommandomodus (siehe 1.1.2) durchgeführt, so steht zusätzlich eine Funktion zur Verfügung (siehe Tabelle 2), in der Versionsdaten ausgelesen und angezeigt werden können. Wird der Endtest zum ersten Mal durchgeführt, so ist diese Funktion auf jeden Fall aufzurufen, da hier eine Verschlüsselung der Festplatten-Seriennummer vorgenommen wird. Wurde "GETCONF" beim Endtest nicht ausgeführt, so kann das Gerät später nicht gestartet werden

Die erzielten Ergebnisse der einzelnen Tests und Informationen zu den Tests werden in beiden Testvarianten über die serielle Schnittstelle ausgegeben. Im kommandogesteuerten Test lassen sich neben den Ausgaben der Ergebnisse zusätzlich die Seriennummern der eingebauten Geräte übertragen. Auf diese Weise können die einzelnen Komponenten einer MediaTV registriert werden.

#### 2. Der Dauertest

Der Dauertest ist mit einem Fernbedienungsautomaten durchzuführen. In dem Test sollen bei laufendem Betriebssystem beispielsweise die Menüfunktionen, sowie z.B. PLAY, RECORD, TIMESHIFT, etc. getestet werden. Zur Kontrolle über die empfangenen und durchgeführten Aktionen werden entsprechende Informationen über die serielle Schnittstelle zur Auswertung ausgegeben.

sequence of the message packages and the compliance with the packet length are checked.

### IR Receiver and Remote Control

Through interaction with the user, this test enables the check of the remote control functions and of the infrared receiver. In doing this, the test programme determines one after the other the individual buttons on the remote control which are to be actuated for remote control.

#### · Indication of the Version Data

If the final test is carried out in the command mode(see 1.1.2), an additional function is available (see Table 2) which allows you to read out and indicate the version data. If the final test is effected for the first time, this function is to be callled up in any case, as it carries out an encoding of the serial number of the hard disk. If "GETCONF" has not been executed during the final test, it is not possible to start the set at a later date.

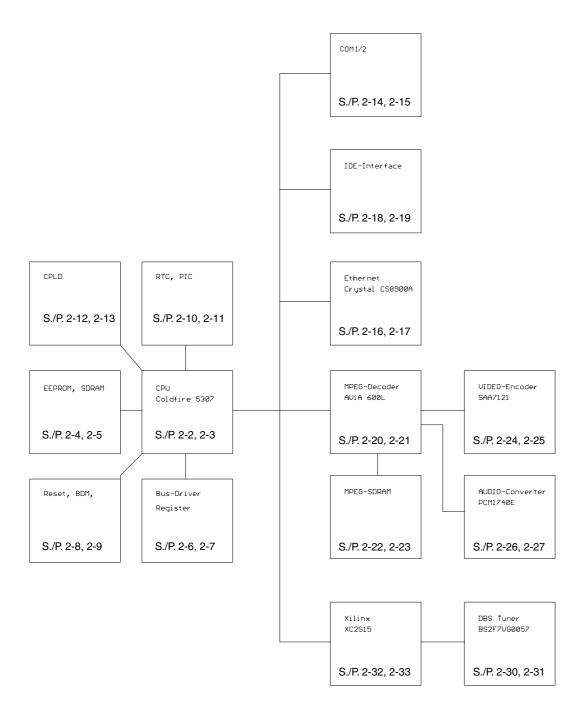
The achieved results of the individual tests and information about the tests are read out for both test variants via the serial interface. For the command-controlled test, the serial numbers of the built-in devices can be transferred in addition to the test results. In this way it is possible to register the individual components of a MediaTV.

#### 2. The Continuous Test

The continuous test is to be carried out with an automatic remote control equipment. With this test, the menu functions as well as PLAY, RECORD, TIMESHIFT, etc., for example, are to be tested with the running operating system. For checking the received and executed actions, corresponding information is output via the serial interface for evaluation.

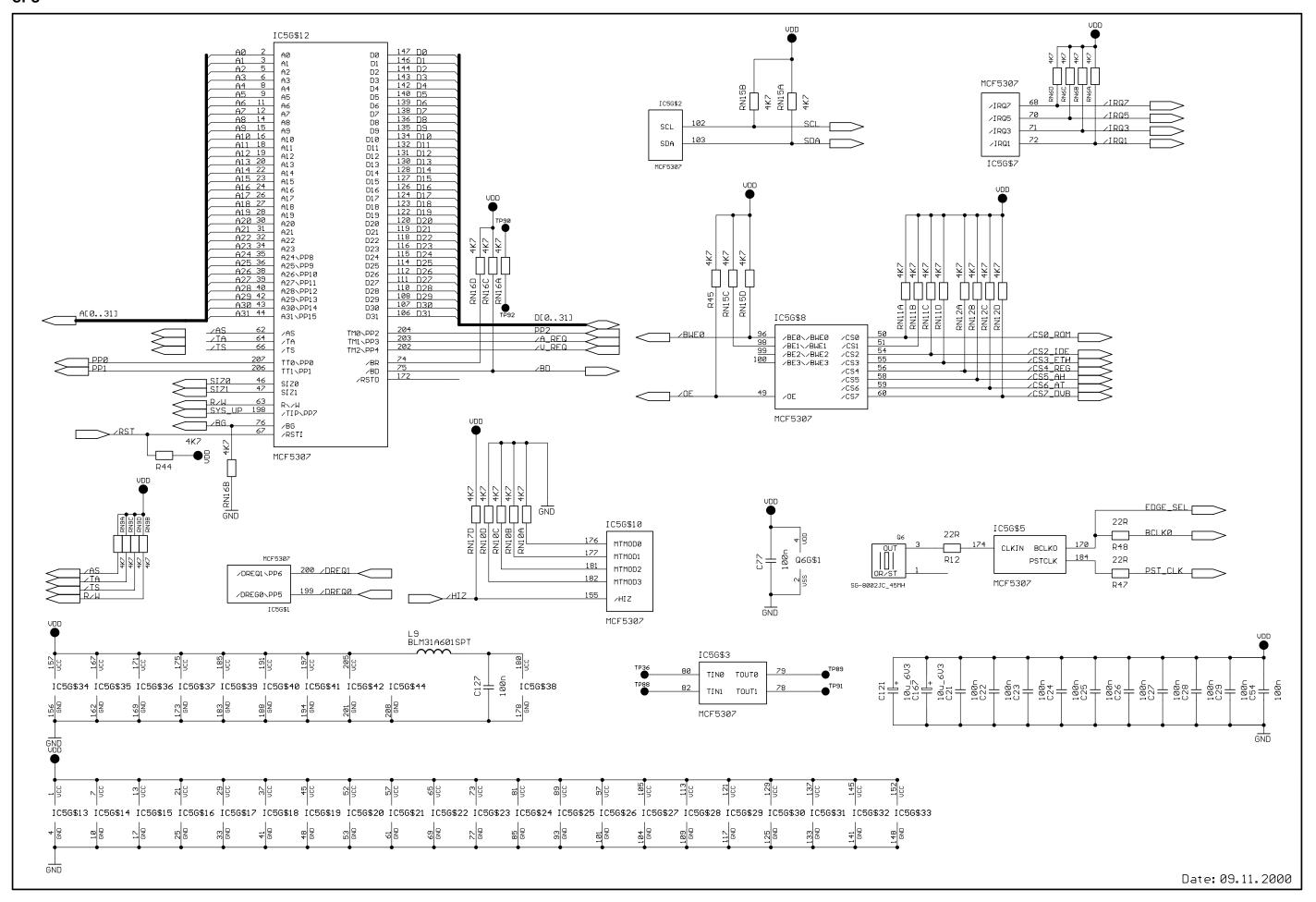
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## Übersicht / Overview



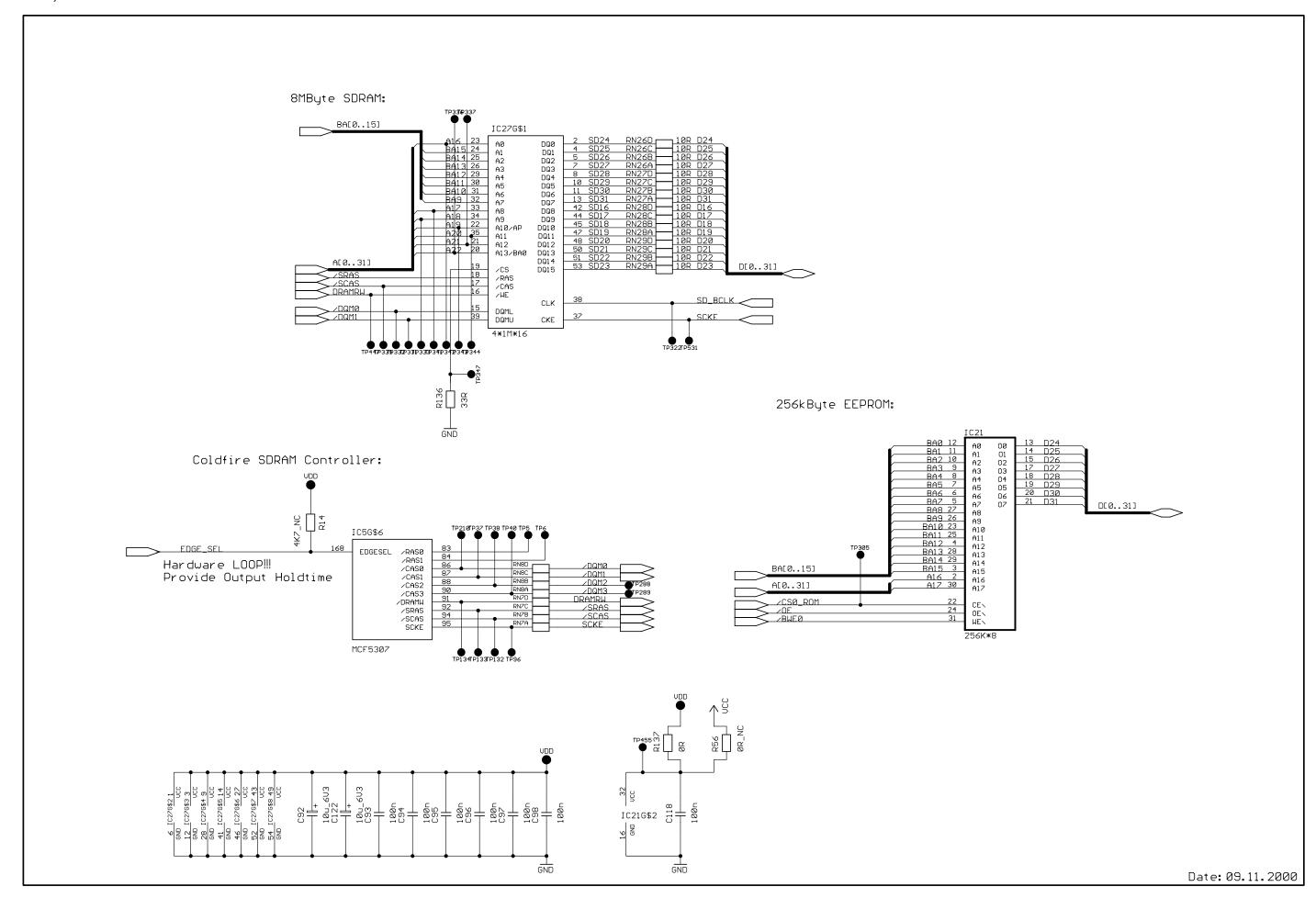
2 - 1 GRUNDIG Service

### **CPU**

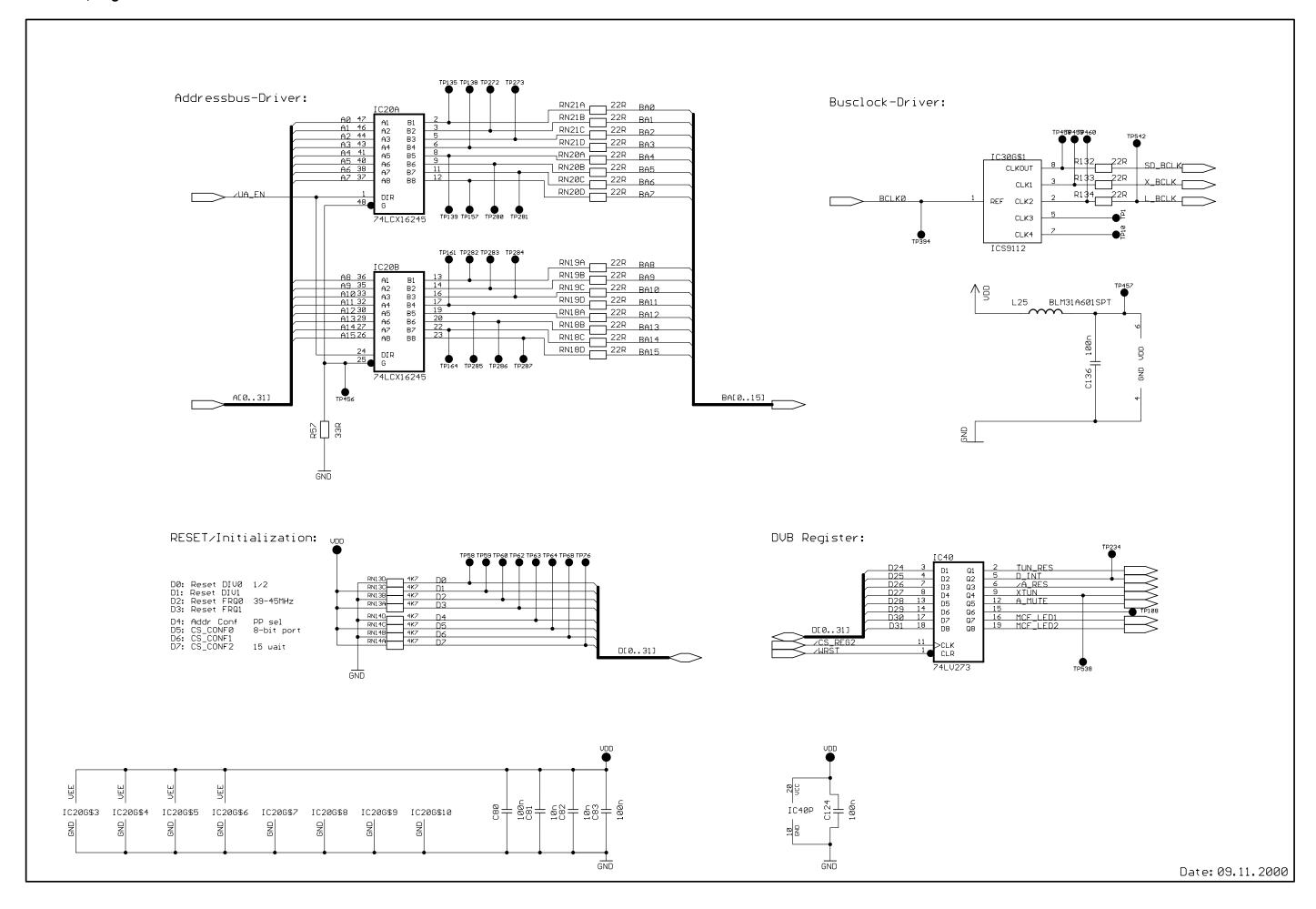


## ROM, SDRAM

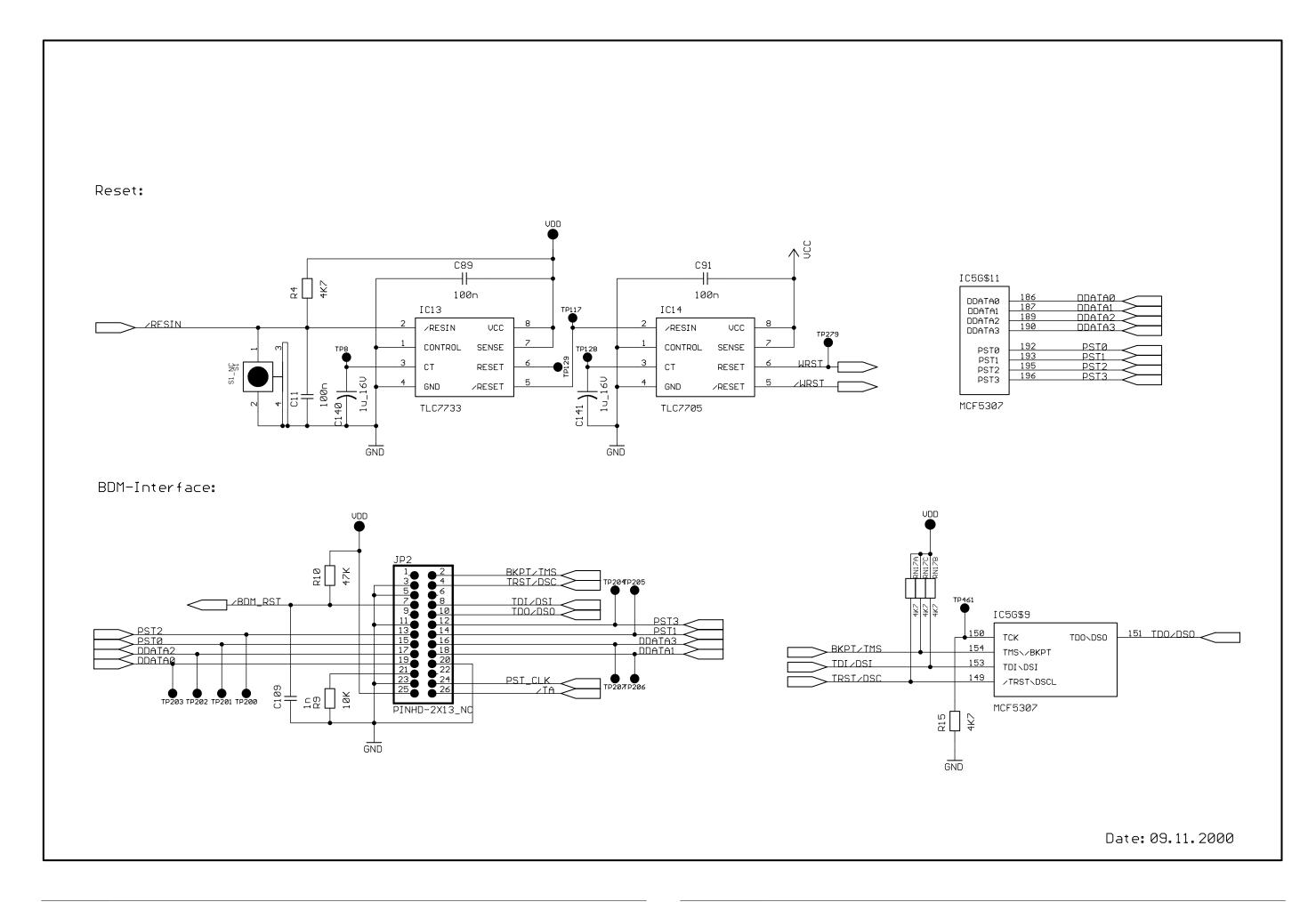
Schaltpläne und Druckplattenabbildungen / Circuit Diagrams and Layout of PCBs



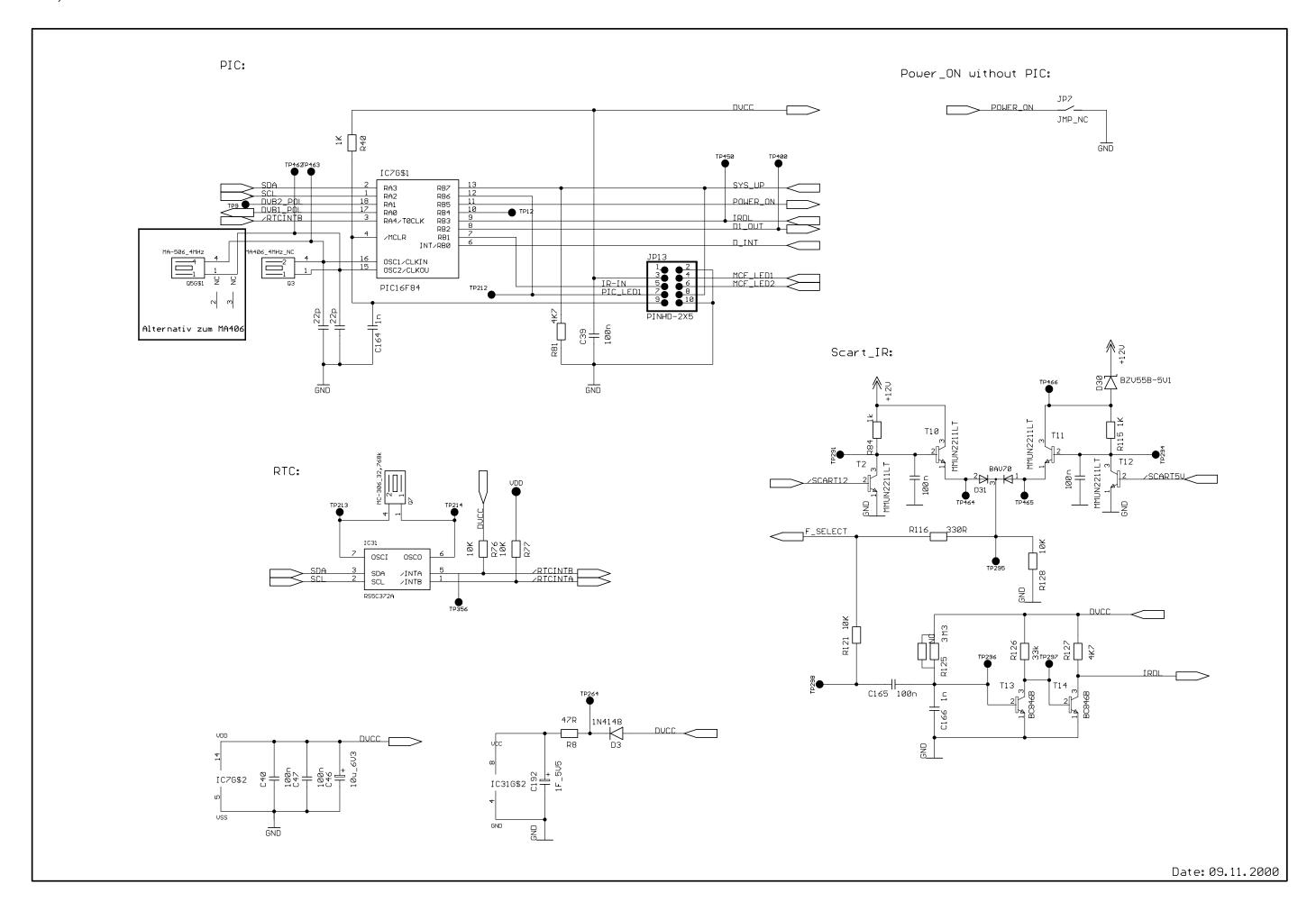
## **Busdriver, Register**



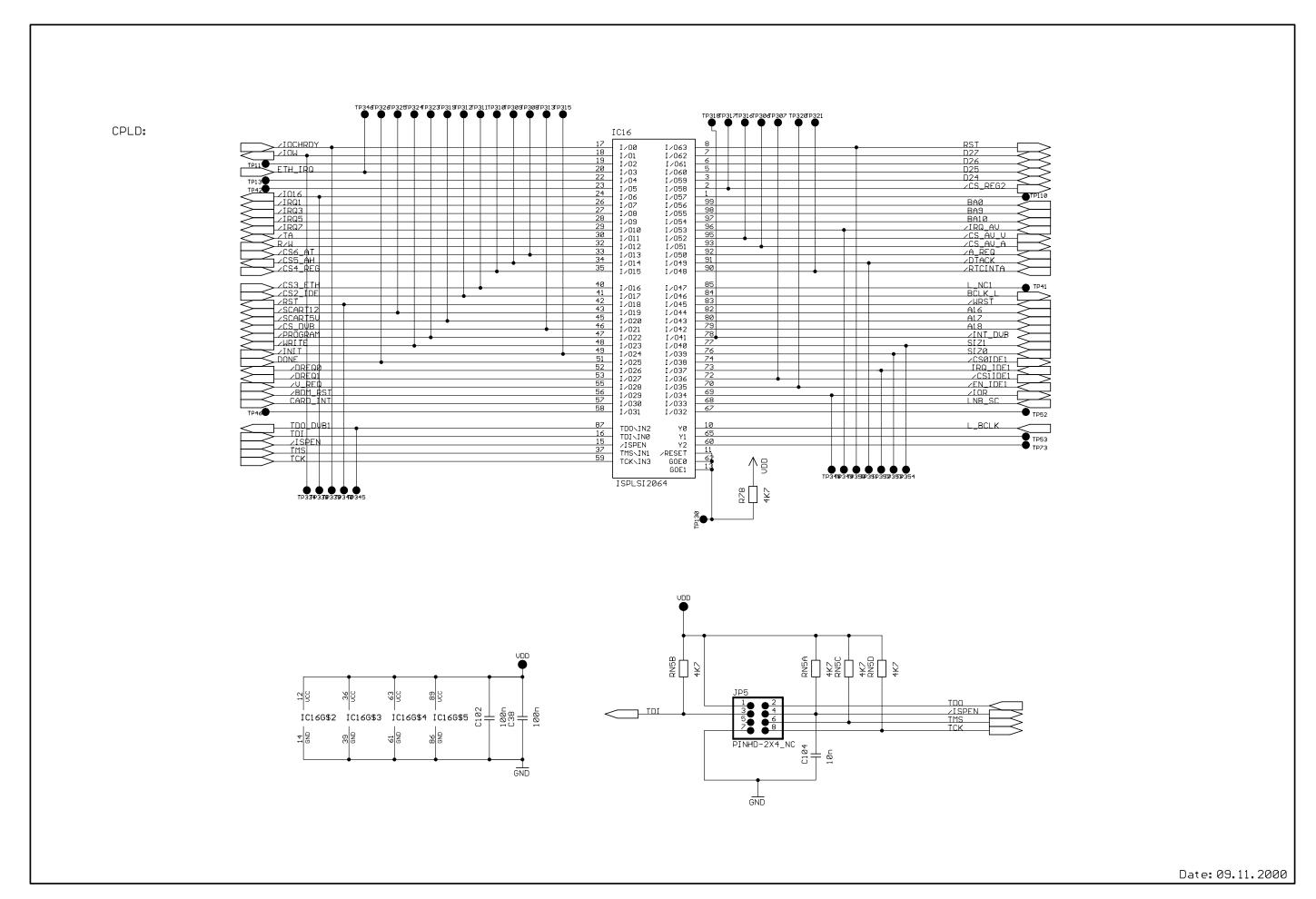
## Reset, BDM



## RTC, PIC



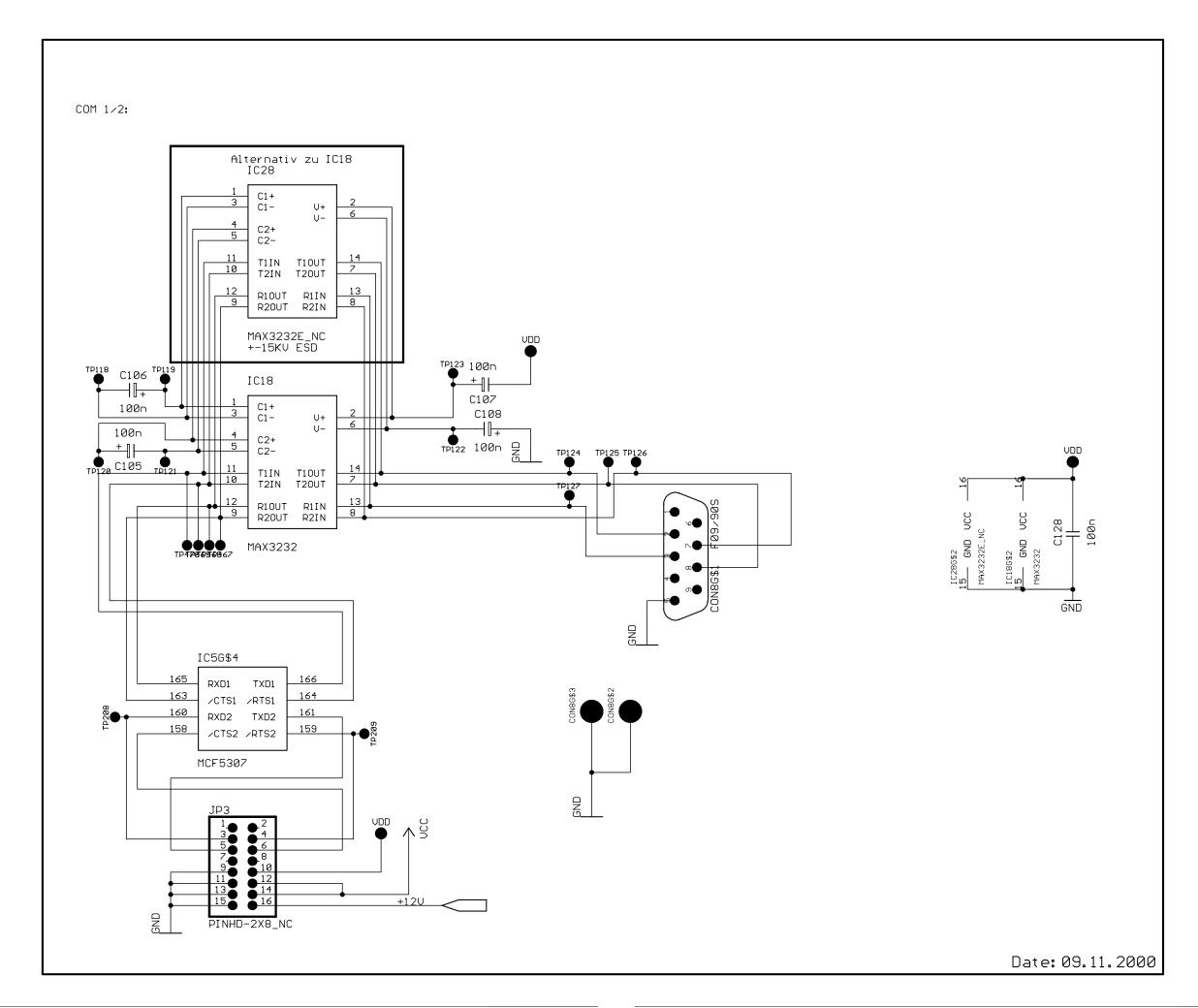
## **CPLD**



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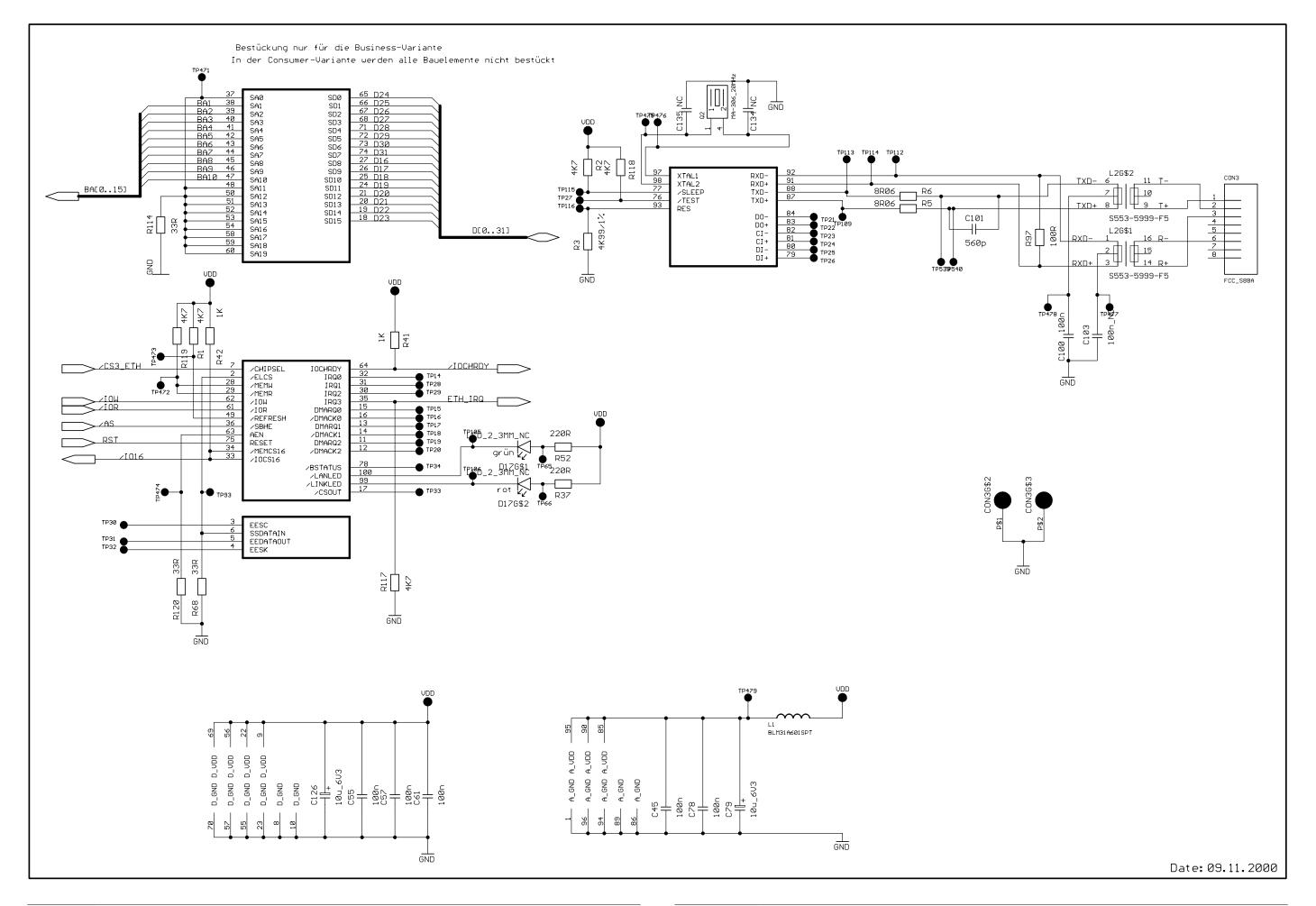
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## COM1/2

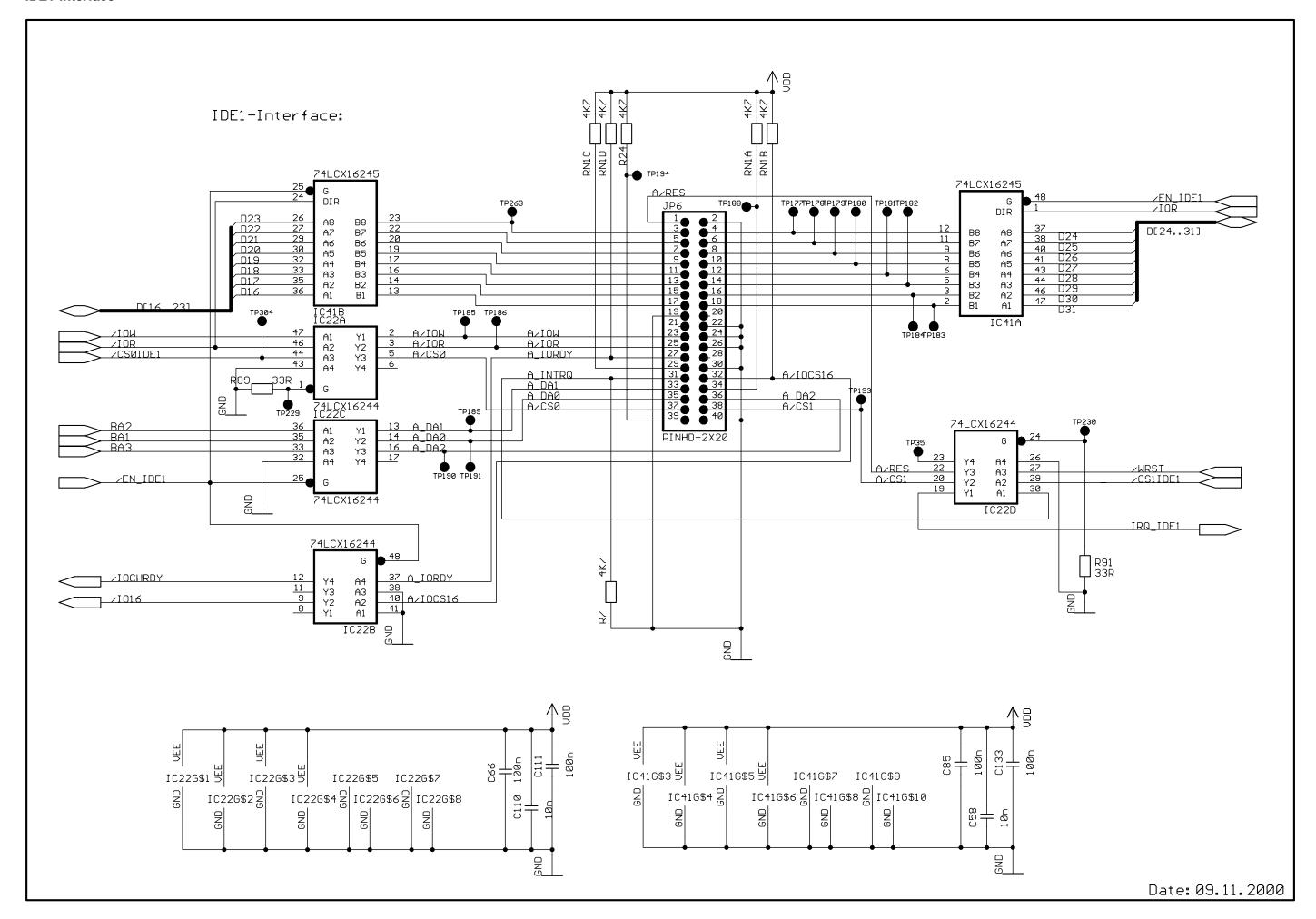


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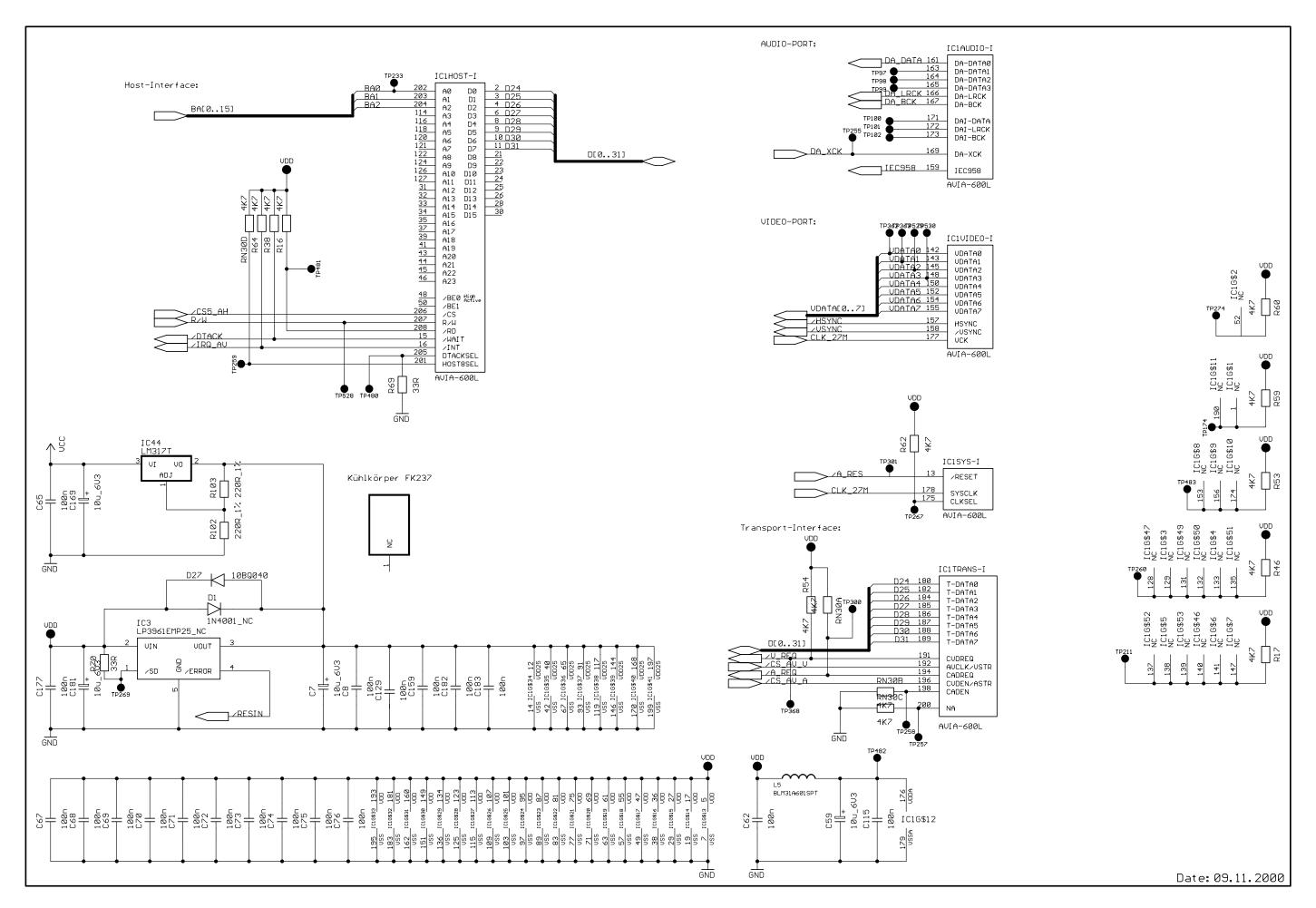
## Ethernet (Option)



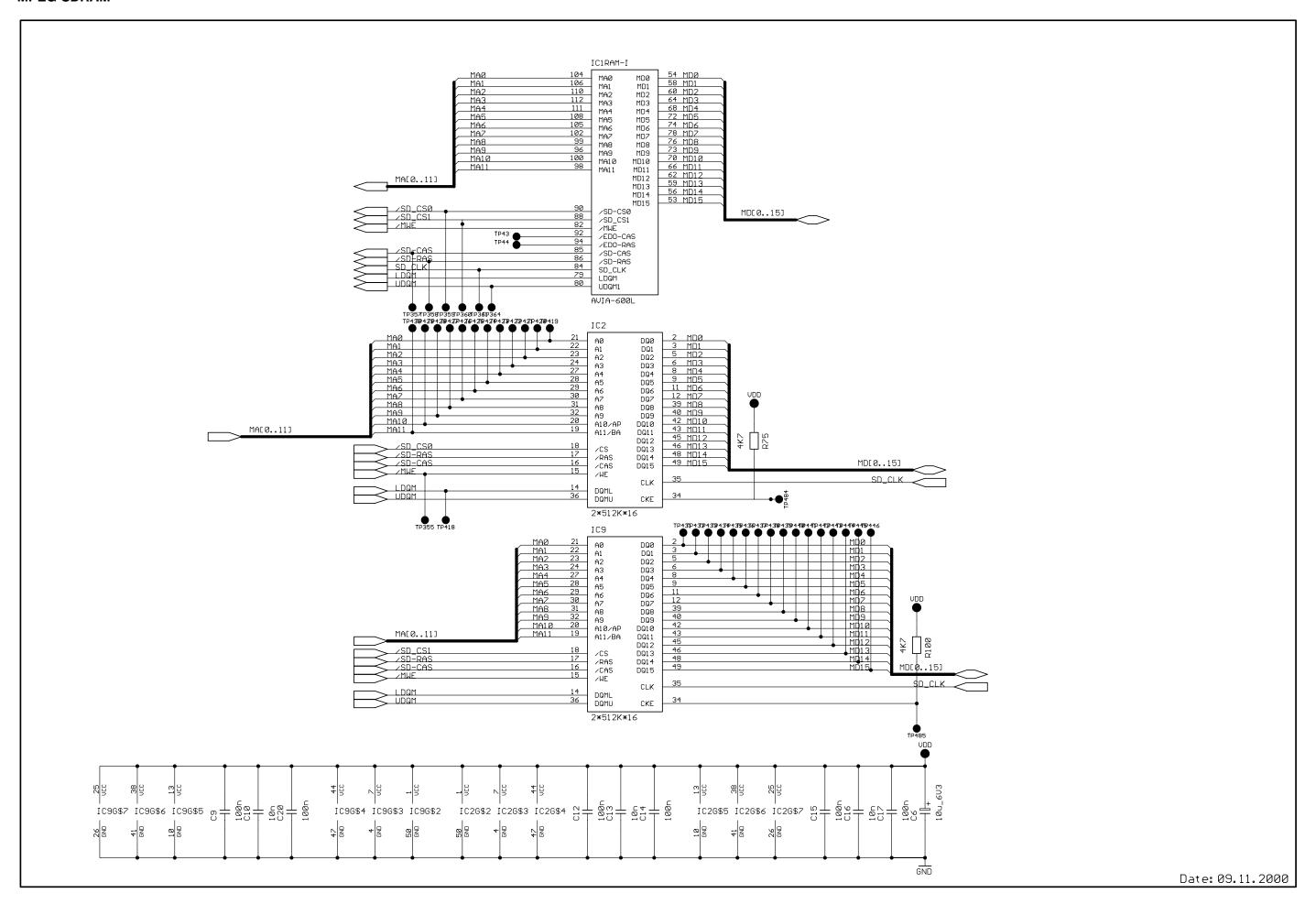
#### **IDE1-Interface**



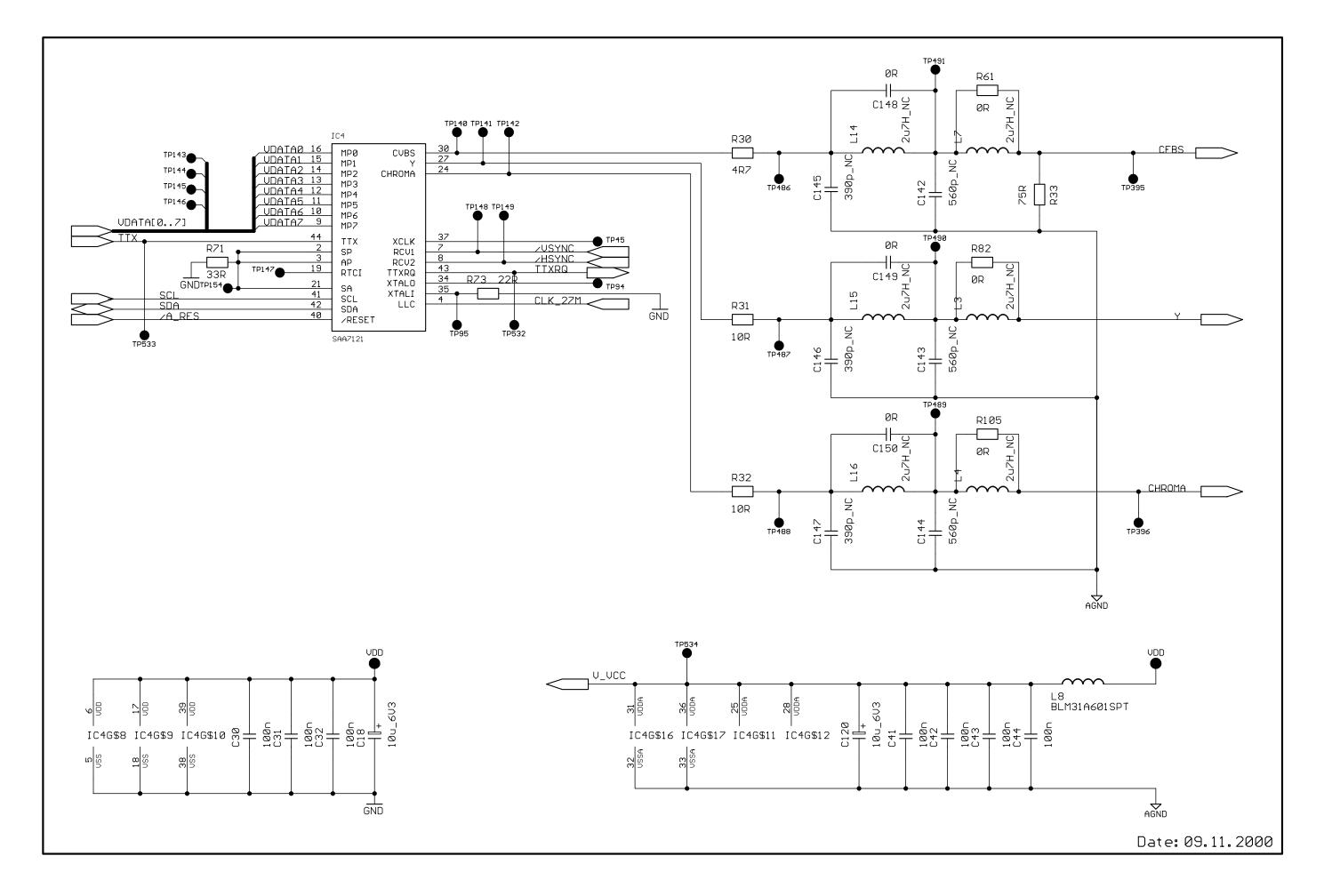
#### **MPEG-Decoder**



### MPEG-SDRAM



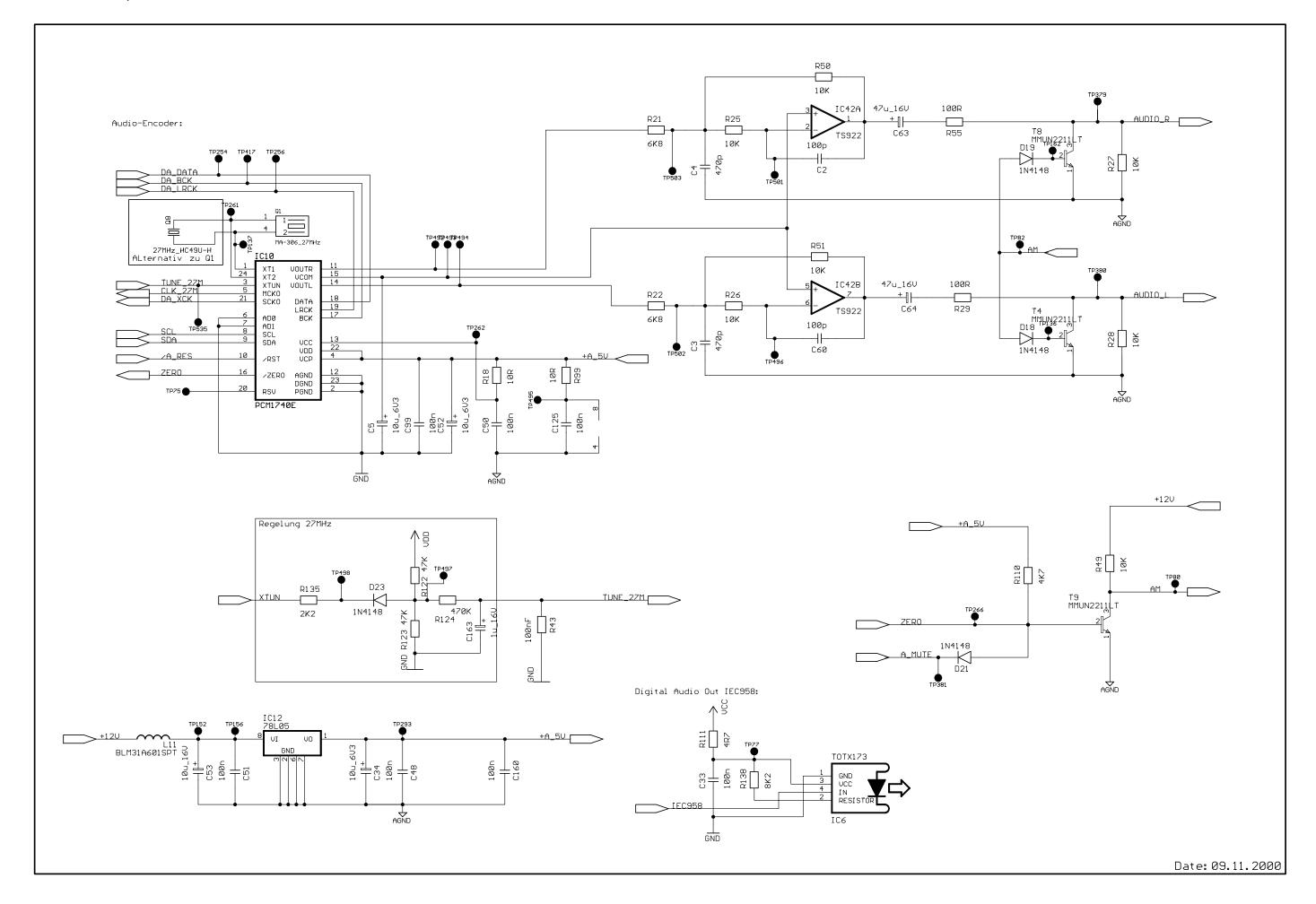
### **Video Encoder**



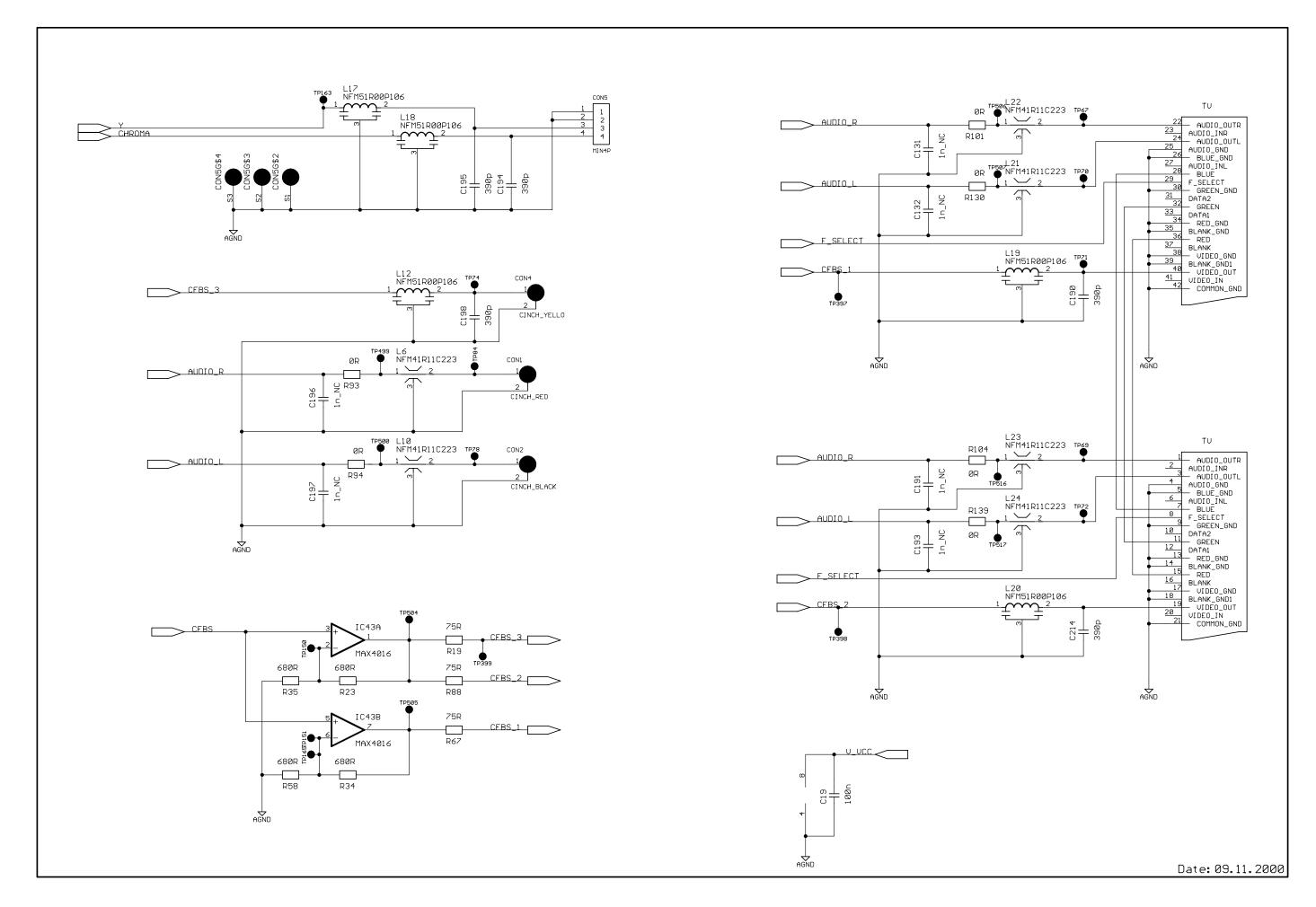
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## **Audio Encoder, IEC958**

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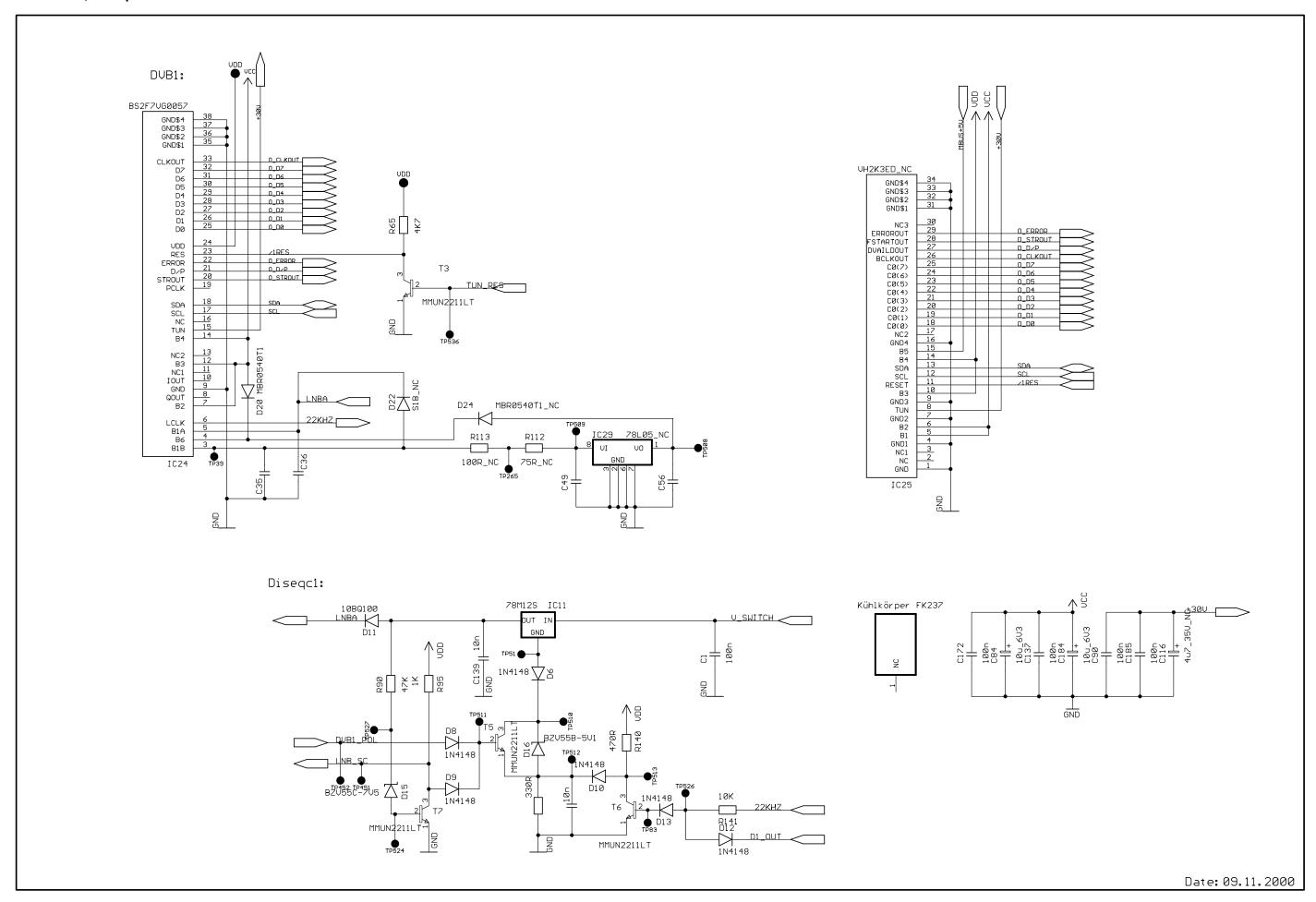


## **Audio/Video Connectors**



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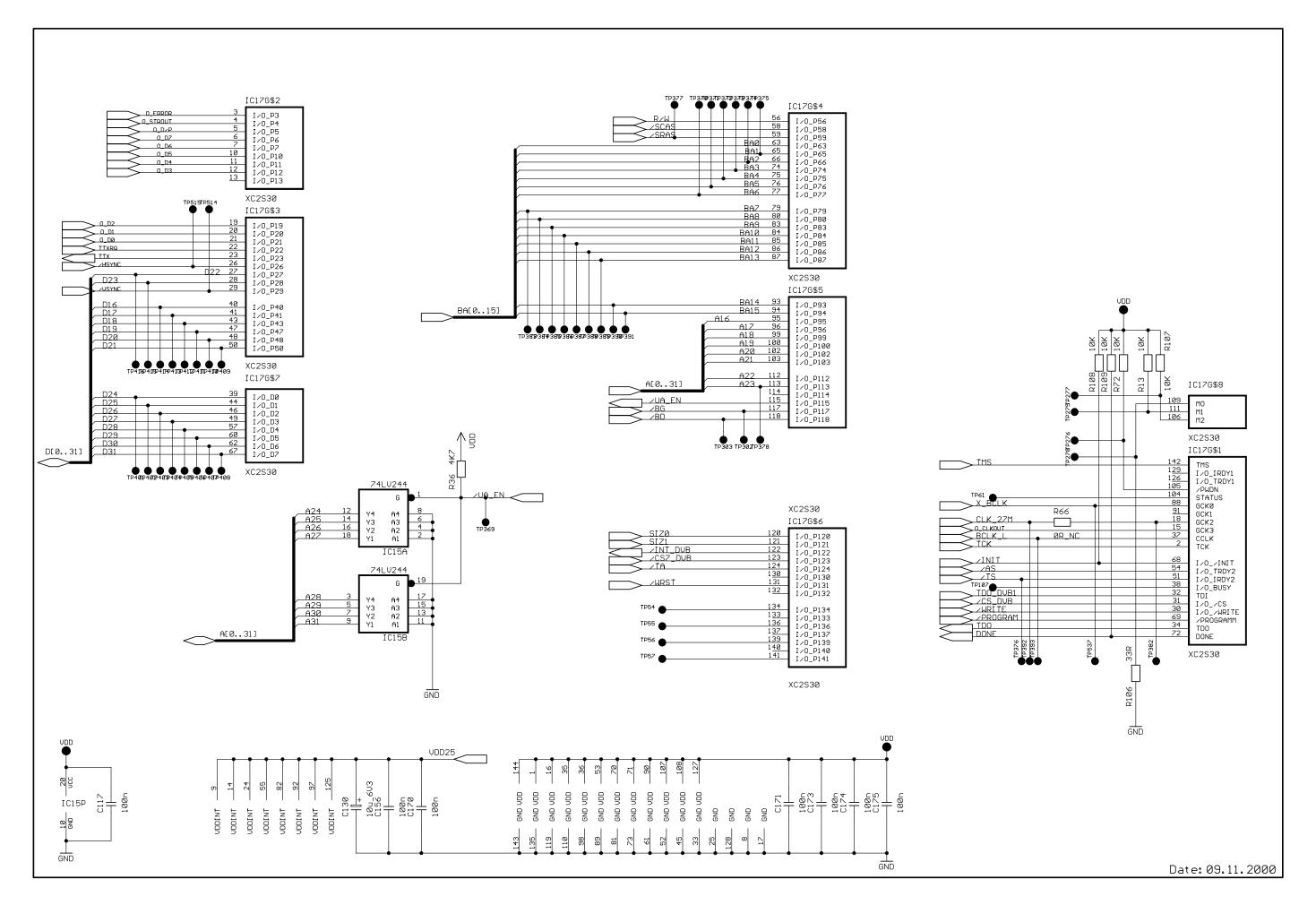
## **DVB Tuner, Diseqc**



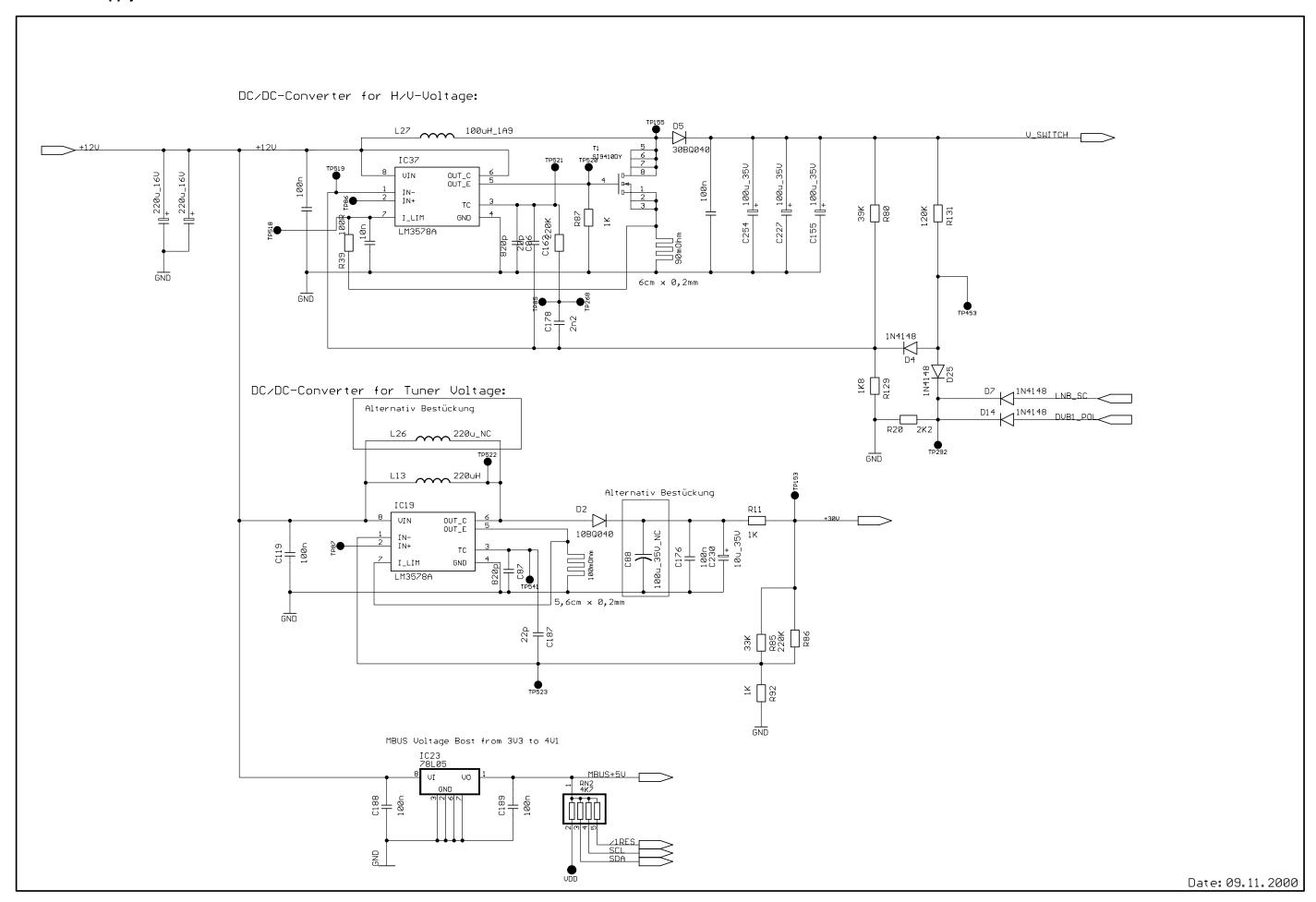
SeleXX PDR 5000 S DIG

Schaltpläne und Druckplattenabbildungen / Circuit Diagrams and Layout of PCBs

#### **DVB Xilinx**

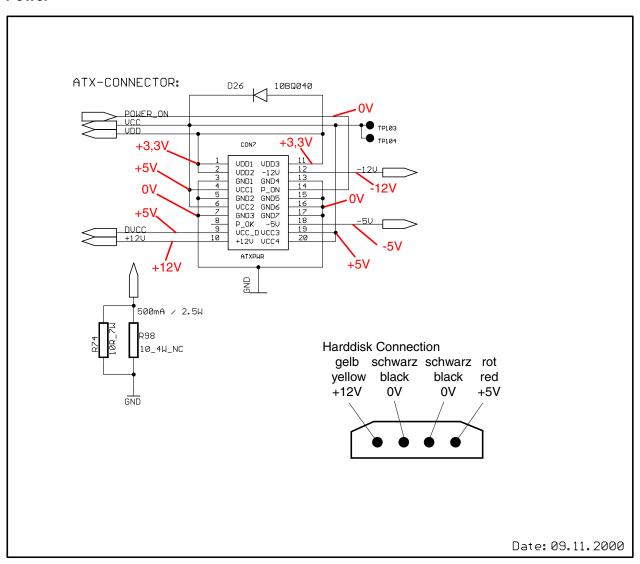


## **DVB Power Supply**

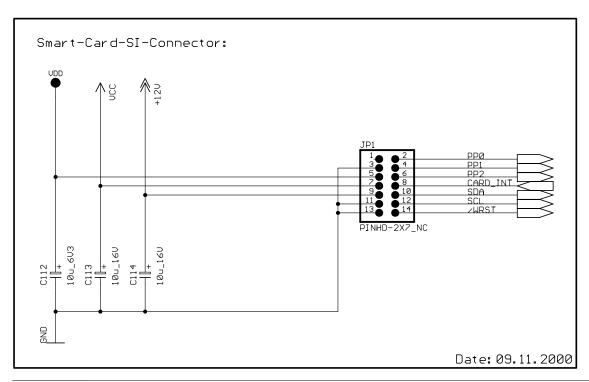


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## Power

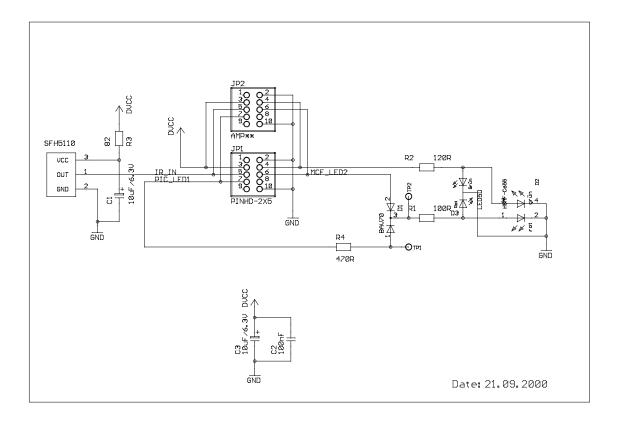


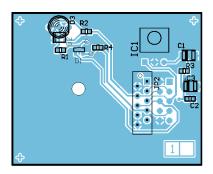
## Smart-Card-SI-Connector (Option)



## **Front Control LED**

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## Hauptplatte / Main PCB

#### 4-Fach-Multilayer-Platte

Die Leiterplatte besteht aus 4 Layern:

Rot - Leiterbahnen der Bestückungsseite, Blau - Leiterbahnen der Lötseite.

Dazwischen liegen noch ein "Betriebsspannungs-Layer" - Gelb und ein "Masse-Layer" - Grün.

Ansicht Bestückungsseite + Layer "Lötseite" / View on component side + layer "Solder Side"

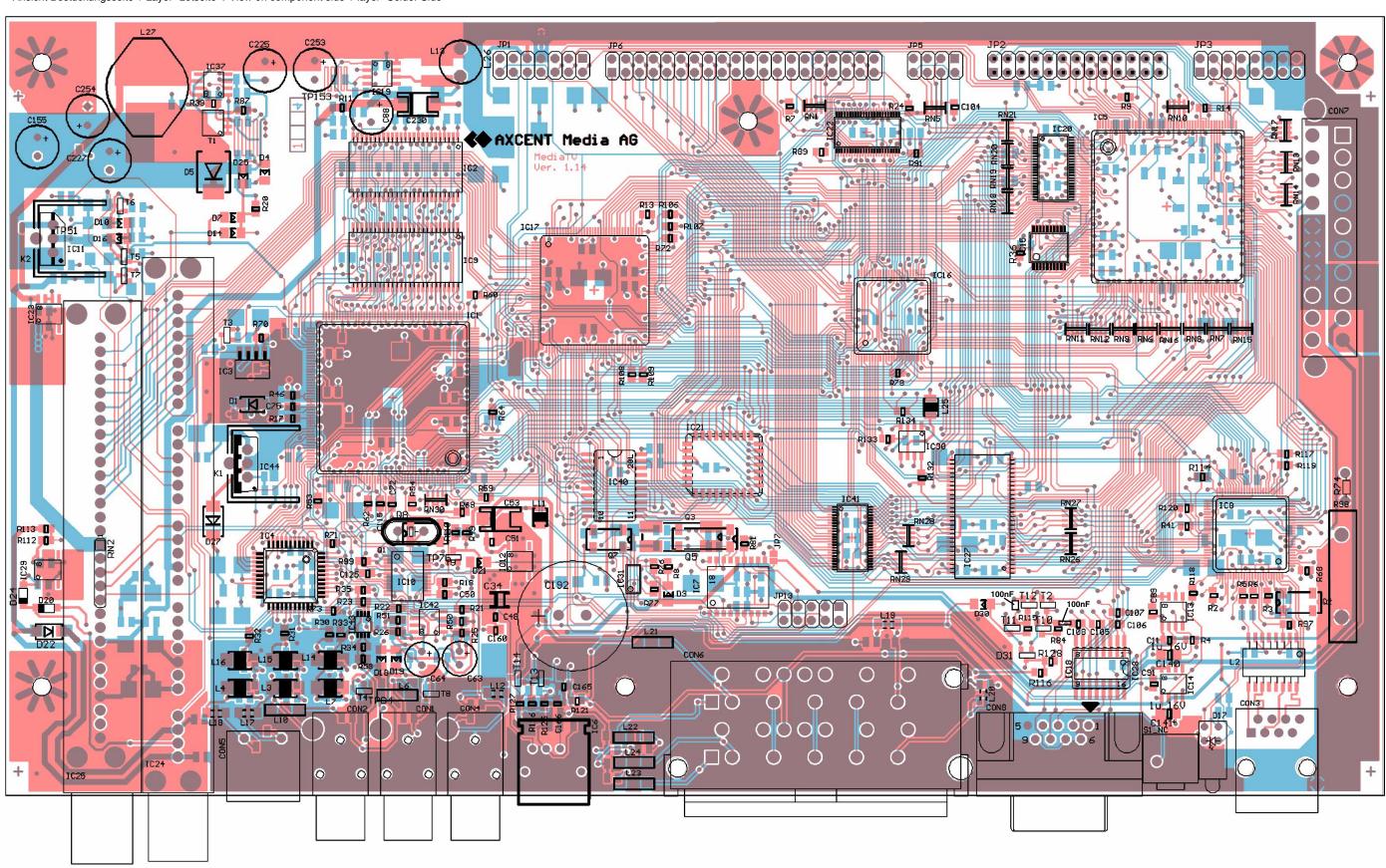
#### 4-Times-Multilayer PCB

Schaltpläne und Druckplattenabbildungen / Circuit Diagrams and Layout of PCBs

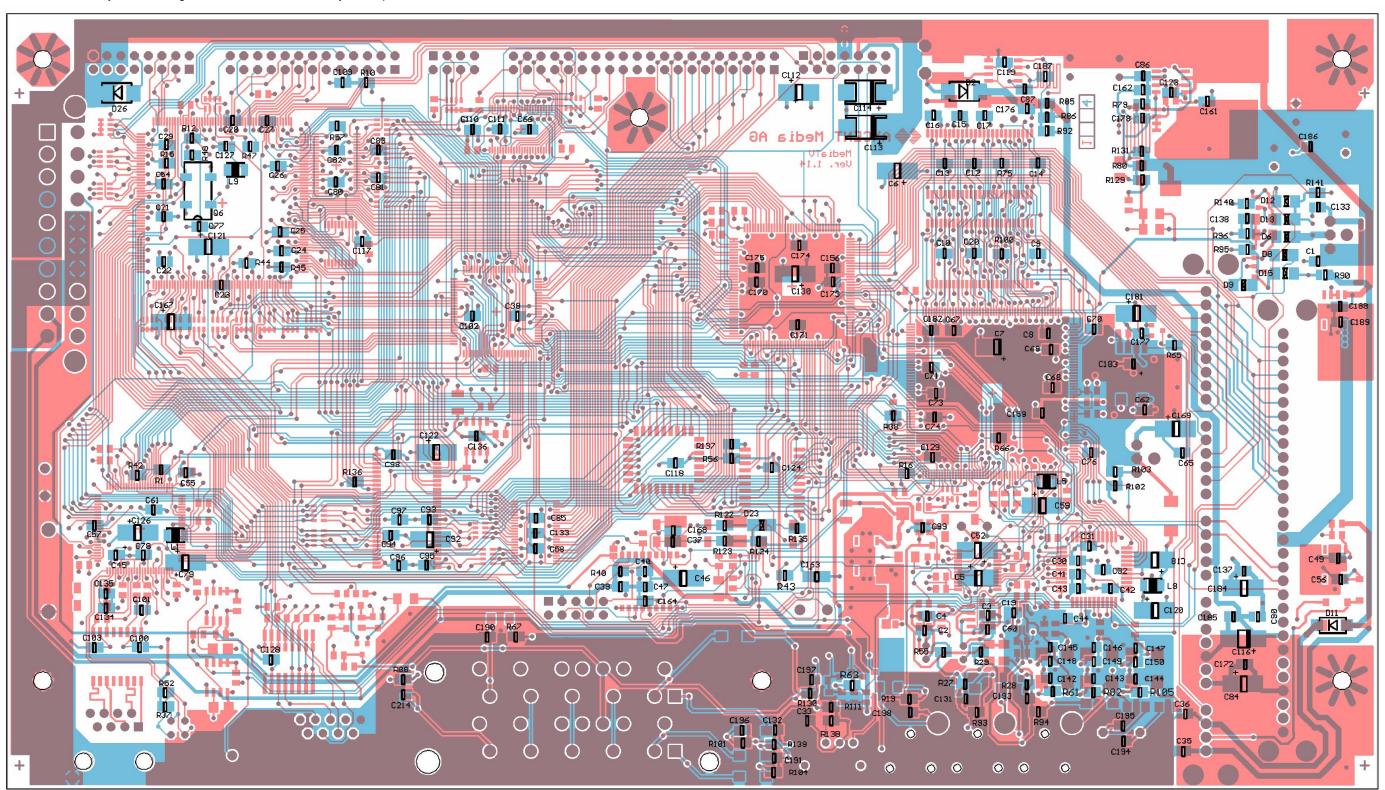
The PCB consists of 4 layers:

Red - wiring of the component side, blue - wiring of the solder side.

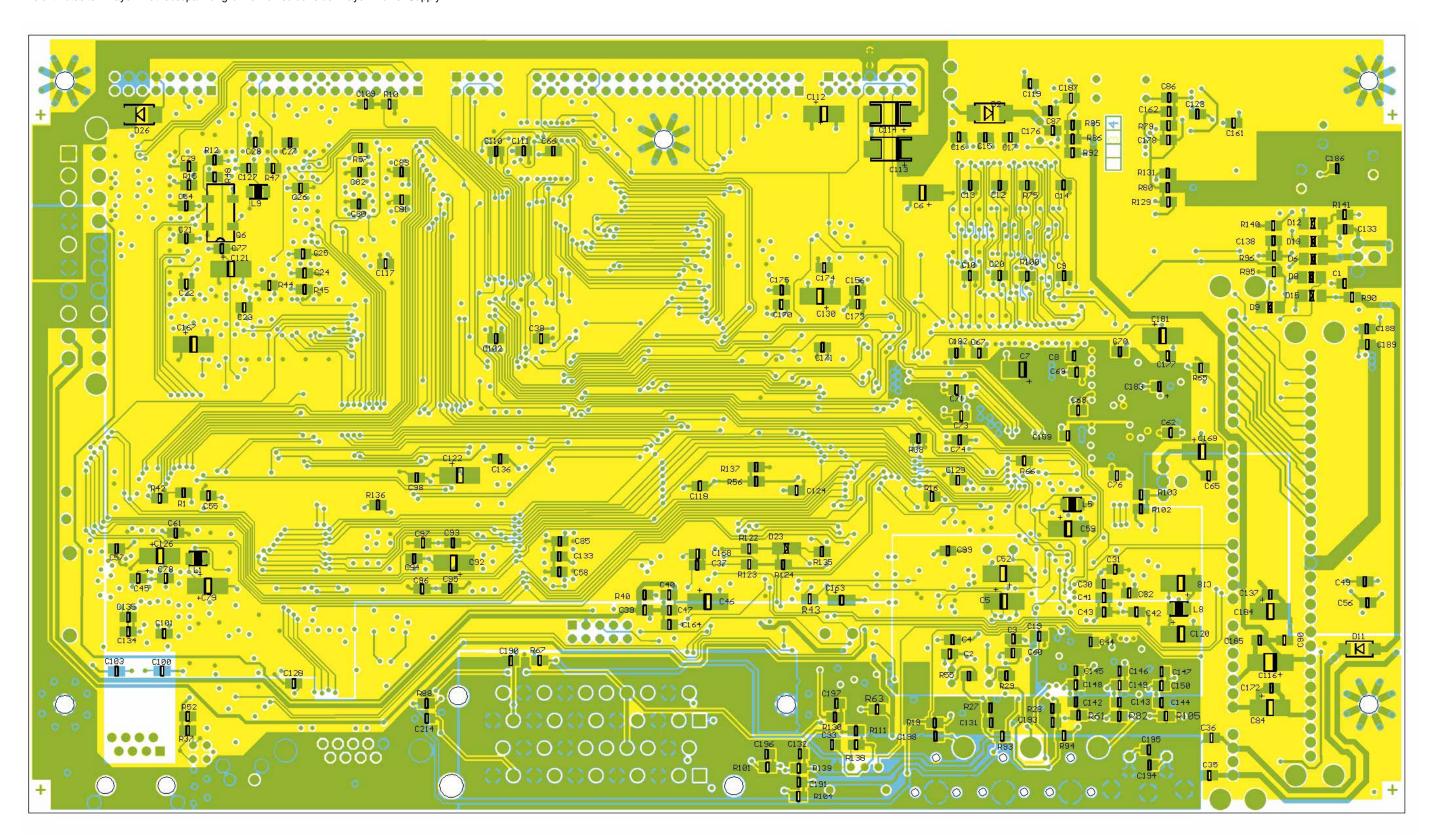
In between there are a "Power Supply" layer - yellow and a "Ground Layer" - green.



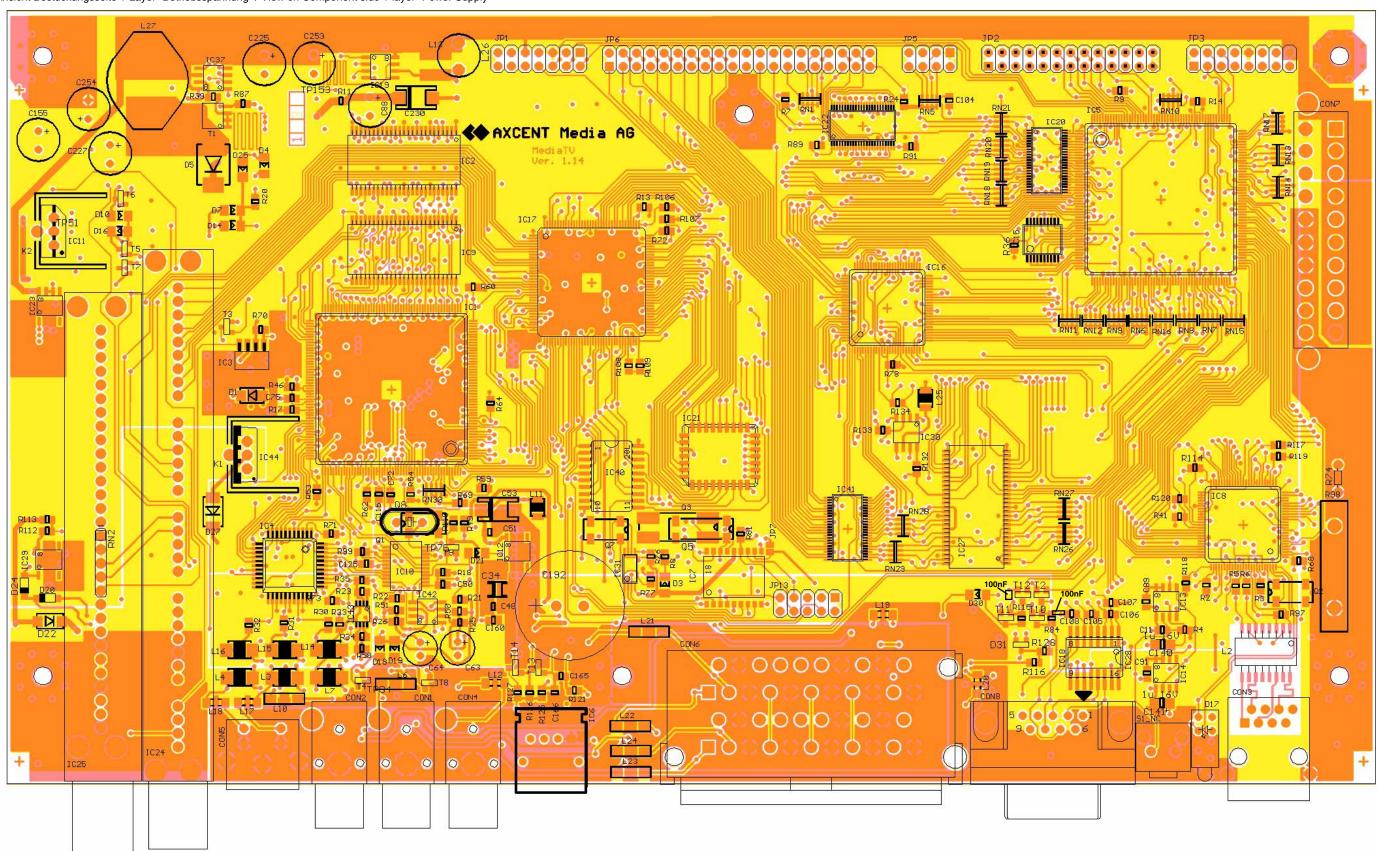
Ansicht Lötseite + Layer "Bestückungsseite" / View on solder side + layer "Component Side"



Ansicht Lötseite + Layer "Betriebsspannung" / View on solder side + layer "Power Supply"

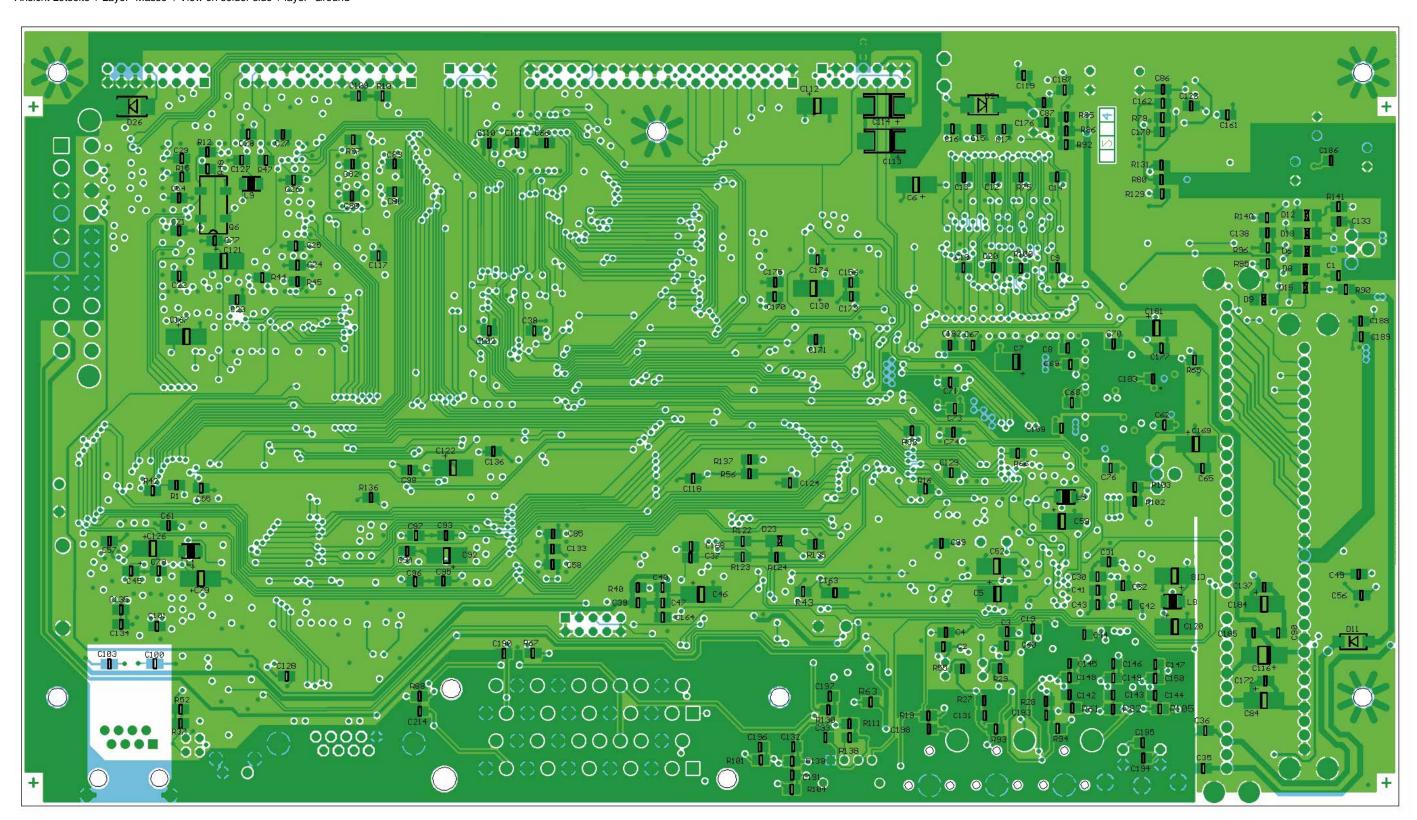


Ansicht Bestückungsseite + Layer "Betriebsspannung" / View on Component side + layer "Power Supply"

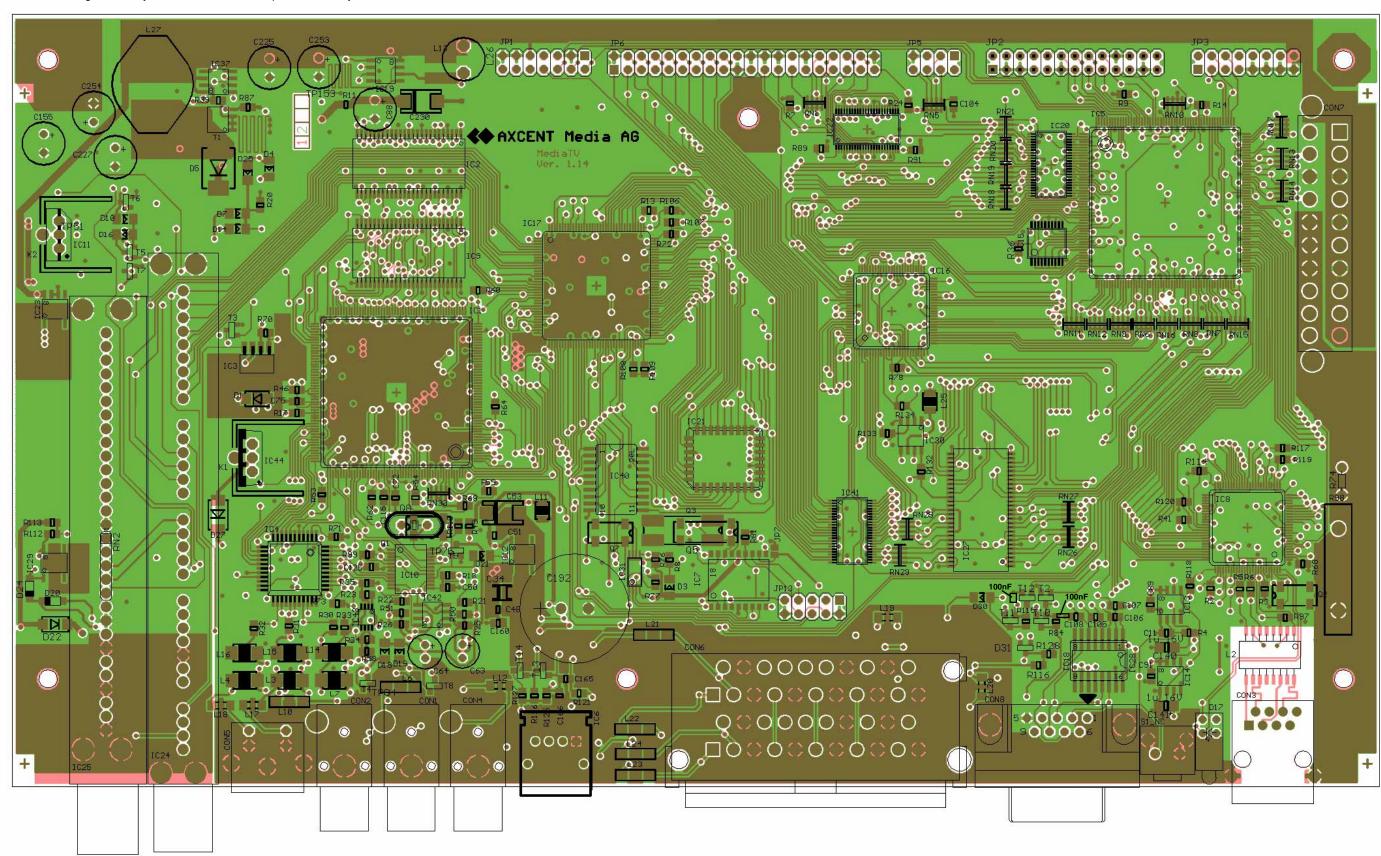


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Ansicht Lötseite + Layer "Masse" / View on solder side + layer "Ground"



Ansicht Bestückungsseite + Layer "Masse" / View on Component side + layer "Ground"



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SeleXX PDR 5000 S DIG Signale / Signale /

Signale	e / Sig	nals	5		Signal	Bauteil Part	Pin	Seite Page	Beschreibung Description
Signal	Bauteil Part	Pin	Seite Page	Beschreibung Description	/BG /BG /BG /BG	IC17 IC5 RN16 TP302	117 76 2 P\$1	2-32,2-33 2-2,2-3 2-2,2-3 2-32,2-33	Bus Grant
+12V +12V +12V +12V +12V +12V	C114 C119 C123 C188 C225 C253	1 2 2 2 1 1	2-36 2-34,2-35 2-34,2-35 2-34,2-35 2-34,2-35 2-34,2-35	Versorgungsspannung	/BWE0 /BWE0 /BWE0 /BWE0	IC21 IC5 RN15 TP79	31 96 4 P\$1	2-4,2-5 2-2,2-3 2-2,2-3 2-2,2-3	Byte Write Enable
+12V +12V +12V +12V +12V	CON7 D30 IC19 IC23	10 2 8 8	2-34,2-35 2-36 2-10,2-11 2-34,2-35 2-34,2-35		/CS0IDE1 /CS0IDE1 /CS0IDE1	IC16 IC22 TP304	74 44 P\$1	2-12,2-13 2-18,2-19 2-18,2-19	Chip Select 0 Festplatte 1
+12V +12V +12V +12V	IC37 IC37 JP1 JP3	6 8 9 16	2-34,2-35 2-34,2-35 2-36 2-14,2-15		/CS0_ROM /CS0_ROM /CS0_ROM /CS0_ROM	IC21 IC5 RN11 TP305	22 50 8 P\$1	2-4,2-5 2-2,2-3 2-2,2-3 2-4,2-5	Chip Select EEPROM
+12V +12V +12V +12V +12V	L11 L13 L26 L27 R49	2 1 1 1 2	2-26,2-27 2-34,2-35 2-34,2-35 2-34,2-35 2-26,2-27		/CS1IDE1 /CS1IDE1 /CS1IDE1	IC16 IC22 TP307	72 29 P\$1	2-12,2-13 2-18,2-19 2-12,2-13	Chip Select 1 Festplatte 1
+12V +12V +12V	R84 T10	2 3 1	2-10,2-11 2-10,2-11 2-30,2-31	Tuner	/CS2_IDE /CS2_IDE /CS2_IDE /CS2_IDE	IC16 IC5 RN11 TP312	41 54 6 P\$1	2-12,2-13 2-2,2-3 2-2,2-3 2-12,2-13	Chip Select Festplatten
+30V +30V +30V +30V +30V +30V	C185 C90 IC24 IC25 R11 R85	2 15 8 2	2-30,2-31 2-30,2-31 2-30,2-31 2-30,2-31 2-34,2-35 2-34,2-35	Versorgungsspannung	/CS3_ETH /CS3_ETH /CS3_ETH /CS3_ETH /CS3_ETH	IC16 IC5 IC8 RN11 TP311	40 55 7 5 P\$1	2-12,2-13 2-2,2-3 2-16,2-17 2-2,2-3 2-12,2-13	Chip Select Ethernet
+30V +30V +A_5V +A_5V	R86 TP153 C160 C34	2	2-34,2-35 2-34,2-35 2-26,2-27 2-26,2-27	Audio Versorgungsspannung	/CS4_REG /CS4_REG /CS4_REG /CS4_REG	IC16 IC5 RN12 TP310	35 56 8 P\$1	2-12,2-13 2-2,2-3 2-2,2-3 2-12,2-13	Chip Select Register
+A_5V +A_5V +A_5V +A_5V +A_5V +A_5V	C48 C52 C99 IC10 IC10 IC12	2 1 2 22 4 1	2-26,2-27 2-26,2-27 2-26,2-27 2-26,2-27 2-26,2-27 2-26,2-27		/CS5_AH /CS5_AH /CS5_AH /CS5_AH /CS5_AH	IC1 IC16 IC5 RN12 TP309	206 34 58 7 P\$1	2-20,2-21 2-12,2-13 2-2,2-3 2-2,2-3 2-12,2-13	Chip Select Avia Host Interface
+A_5V +A_5V +A_5V +A_5V	R110 R18 R99 TP293		2-26,2-27 2-26,2-27 2-26,2-27 2-26,2-27		/CS6_AT /CS6_AT /CS6_AT /CS6_AT	IC16 IC5 RN12 TP308	33 59 6 P\$1	2-12,2-13 2-2,2-3 2-2,2-3 2-12,2-13	Chip Select Avia Transport Interface
-5V -12V	CON7	18 12	2-36 2-36	Versorgungsspannung Versorgungsspannung	/CS7_DVB /CS7_DVB /CS7_DVB	IC17 IC5 RN12	123 60 5	2-32,2-33 2-2,2-3 2-2,2-3	Chip Select Xilinx Initialisierung
/1RES /1RES /1RES /1RES /1RES	IC24 IC25 R65 RN2 T3	23 11 1 5 3	2-30,2-31 2-30,2-31 2-30,2-31 2-34,2-35 2-30,2-31	Tuner Reset	/CS7_DVB /CS_AV_A /CS_AV_A /CS_AV_A	TP314 IC1 IC16 TP306	196 93	2-2,2-3 2-20,2-21 2-12,2-13 2-12,2-13	Data Strobe Audio Stream Avia Transport Interface
/AS /AS /AS /AS	IC17 IC5 IC8 RN9	54 62 36 8	2-32,2-33 2-2,2-3 2-16,2-17 2-2,2-3	Address Strobe	/CS_AV_V /CS_AV_V /CS_AV_V	IC1 IC16 TP316	95	2-20,2-21 2-12,2-13 2-12,2-13	Data Strobe Video Stream Avia Transport Interface
/AS /A_REQ	TP299 IC1	P\$1 194	2-2,2-3 2-20,2-21	Audio Request	/CS_DVB /CS_DVB /CS_DVB	IC16 IC17 TP313	46 31 P\$1	2-12,2-13 2-32,2-33 2-12,2-13	Chip Select DVB Modul
/A_REQ /A_REQ /A_REQ /A_REQ	IC16 IC5 RN30 TP300	8	2-12,2-13 2-2,2-3 2-20,2-21 2-20,2-21		/CS_REG2 /CS_REG2 /CS_REG2	IC16 IC40 TP317	2 11 P\$1	2-12,2-13 2-6,2-7 2-12,2-13	Chip Select DVB Register
/A_RES /A_RES /A_RES /A_RES	IC1 IC10 IC4 IC40	13 10 40 6	2-20,2-21 2-26,2-27 2-24,2-25 2-6,2-7	Reset Avia/Audio/Video	/DQM0 /DQM0 /DQM0	IC27 RN8 TP332	15 5 P\$1	2-4,2-5 2-4,2-5 2-4,2-5	Data I/O Mask SPEICHER Lower Byte
/A_RES /BD /BD	TP301 IC17 IC5	75	2-32,2-33 2-2,2-3	Bus Driven	/DQM1 /DQM1 /DQM1	IC27 RN8 TP331		2-4,2-5 2-4,2-5 2-4,2-5	Data I/O Mask SPEICHER Upper Byte
/BD /BD	RN16 TP303	3 P\$1	2-2,2-3 2-32,2-33		/DQM2 /DQM2	RN8 TP288	7 P\$1	2-4,2-5 2-4,2-5	Data I/O Mask SPEICHER free
/BDM_RST /BDM_RST /BDM_RST	C109 IC16 JP2	2 56 7	2-8,2-9 2-12,2-13 2-8,2-9	Reset BDM_Interface	/DQM3 /DQM3	RN8 TP289	8 P\$1	2-4,2-5 2-4,2-5	Data I/O Mask SPEICHER free
/BDM_RST	R10	1	2-8,2-9		/DREQ0	IC16	52	2-12,2-13	DMA Transfer Request(0)

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Signale / Signals SeleXX PDR 5000 S DIG

Signal	Bauteil Part	Pin	Seite Page	Beschreibung Description	Signal	Bauteil Part	Pin	Seite Page	Beschreibung Description
/DREQ0 /DREQ0	IC5 TP327	199 P\$1	2-2,2-3 2-2,2-3		/ISPEN /ISPEN	C104 IC16	2 15	2-12,2-13 2-12,2-13	Lattice Enable vom Programmierstecker
/DREQ1 /DREQ1 /DREQ1	IC16 IC5 TP328		2-12,2-13 2-2,2-3 2-2,2-3	DMA Transfer Request(1)	/ISPEN /ISPEN	JP5 RN5	4 1	2-12,2-13 2-12,2-13	
/DTACK /DTACK /DTACK /DTACK	IC1 IC16 R38 TP351	15 91 1 P\$1	2-20,2-21 2-12,2-13 2-20,2-21 2-12,2-13	Data acknowledge Avia Host Interface	/MWE /MWE /MWE /MWE	IC1 IC2 IC9 TP355	82 15 15 P\$1	2-22,2-23 2-22,2-23 2-22,2-23 2-22,2-23	Memory Write Enable Avia Speicher
/EN_IDE1 /EN_IDE1 /EN_IDE1 /EN_IDE1	IC16 IC22 IC22 IC41	70 25 48 25	2-12,2-13 2-18,2-19 2-18,2-19 2-18,2-19	Enable Festplatte	/OE /OE /OE /OE	IC21 IC5 R45 TP330	24 49 2 P\$1	2-4,2-5 2-2,2-3 2-2,2-3 2-2,2-3	Output Enable EEPROM
/EN_IDE1 /EN_IDE1	IC41 TP320	48 P\$1	2-18,2-19 2-12,2-13		/PROGRAM /PROGRAM /PROGRAM	IC16 IC17 TP323	47 69 P\$1	2-12,2-13 2-32,2-33 2-12,2-13	Programmierung Xilinx
/HIZ /HIZ /HIZ	IC5 RN17 TP217	5	2-2,2-3 2-2,2-3 2-2,2-3	High Impedance für alle Ausgänge Coldfire	/RESIN /RESIN /RESIN	C11 IC13 IC3	2 2 4	2-8,2-9 2-8,2-9 2-20,2-21	Reset Eingang (Taster)
/HSYNC /HSYNC /HSYNC	IC1 IC17 IC4	157 26 8	2-32,2-33 2-24,2-25	horizontale Synchr. Video Signal	/RESIN /RESIN	R4 S1	1 1	2-8,2-9 2-8,2-9	Decet
/HSYNC /HSYNC /INIT	TP149 TP515 IC16	P\$1 P\$1 49	,	Initialisierung Xilinx	/RST /RST /RST /RST	IC16 IC5 R44 TP340	42 67 2 P\$1	2-12,2-13 2-2,2-3 2-2,2-3 2-12,2-13	Reset
/INIT /INIT /INIT	IC17 R108 TP315	68 1 P\$1	2-32,2-33 2-32,2-33 2-12,2-13		/RTCINTA /RTCINTA /RTCINTA	IC16 IC31 R77	90 1 2	2-12,2-13 2-10,2-11 2-10,2-11	RTC Alarm A Abschalten
/INT_DVB /INT_DVB /INT_DVB	IC16 IC17 TP318	78 122 P\$1	2-12,2-13 2-32,2-33 2-12,2-13	Interrupt DVB Modul	/RTCINTA /RTCINTB /RTCINTB	TP321 IC31 IC7	P\$1 5 3	2-12,2-13 2-10,2-11 2-10,2-11	RTC Alarm B Einschalten
/IO16 /IO16 /IO16 /IO16	IC16 IC22 IC8 IC8	24 9 33 34	2-12,2-13 2-18,2-19 2-16,2-17 2-16,2-17	Adresse erkannt	/RTCINTB /RTCINTB /SCART5V	R76 TP356	2	2-10,2-11 2-10,2-11 2-12,2-13	Video Modus 4:3
/IO16 /IO16	R42 TP338	2 P\$1	2-16,2-17		/SCART5V /SCART5V	T12 TP319	2	2-10,2-11 2-12,2-13	
/IOCHRDY /IOCHRDY /IOCHRDY /IOCHRDY	IC16 IC22 IC8 R41	17 12 64 2	2-12,2-13 2-18,2-19 2-16,2-17 2-16,2-17	IO Channel Ready	/SCART12 /SCART12 /SCART12	IC16 T2 TP325	43 2 P\$1	2-12,2-13 2-10,2-11 2-12,2-13	Video Modus 16:9
/IOCHRDY /IOR /IOR	TP339 IC16 IC22	P\$1 69 46	2-12,2-13 2-12,2-13 2-18,2-19	I/O Read	/SCAS /SCAS /SCAS /SCAS	IC17 IC27 RN7 TP335	58 17 7 P\$1	2-32,2-33 2-4,2-5 2-4,2-5 2-4,2-5	CAS-Signal SPEICHER
/IOR /IOR /IOR /IOR	IC41 IC41 IC8 TP348	1 24 61 P\$1	2-18,2-19 2-18,2-19 2-16,2-17 2-12,2-13		/SD-CAS /SD-CAS /SD-CAS	IC1 IC2 IC9	85 16 16	2-22,2-23 2-22,2-23 2-22,2-23	CAS-Signal SPEICHER Avia
/IOW /IOW /IOW	IC16 IC22 IC8	18 47 62	2-12,2-13 2-18,2-19 2-16,2-17	I/O Write	/SD-CAS /SD-RAS /SD-RAS	TP357 IC1 IC2	86 17	2-22,2-23 2-22,2-23 2-22,2-23	RAS-Signal SPEICHER Avia
/IOW /IRQ1 /IRQ1	TP334 IC16 IC5	P\$1 26 72	2-12,2-13 2-2,2-3	Interrupt Request 1	/SD-RAS /SD-RAS /SD_CS0	IC9 TP358 IC1	17 P\$1 90	2-22,2-23	Chip Select 0
/IRQ1 /IRQ1	RN6 TP47	1 P\$1	2-2,2-3 2-2,2-3		/SD_CS0 /SD_CS0	IC2 TP359	18 P\$1	2-22,2-23 2-22,2-23	Avia SPEICHER
/IRQ3 /IRQ3 /IRQ3 /IRQ3	IC16 IC5 RN6 TP48	27 71 2 P\$1	2-12,2-13 2-2,2-3 2-2,2-3 2-2,2-3	Interrupt Request 3	/SD_CS1 /SD_CS1 /SD_CS1	IC1 IC9 TP360	88 18 P\$1	2-22,2-23 2-22,2-23 2-22,2-23	Chip Select 1 Avia SPEICHER
/IRQ5 /IRQ5 /IRQ5 /IRQ5	IC16 IC5 RN6 TP329	28 70 3 P\$1	2-12,2-13 2-2,2-3 2-2,2-3 2-2,2-3	Interrupt Request 5	/SRAS /SRAS /SRAS /SRAS	IC17 IC27 RN7 TP377	59 18 6 P\$1	2-32,2-33 2-4,2-5 2-4,2-5 2-32,2-33	RAS-Signal SPEICHER
/IRQ7 /IRQ7 /IRQ7 /IRQ7	IC16 IC5 RN6 TP218	29 68 4 P\$1	2-12,2-13 2-2,2-3 2-2,2-3 2-2,2-3	Interrupt Request 7	/TA /TA /TA /TA /TA	IC16 IC17 IC5 JP2 RN9	30 124 64 26 6	2-12,2-13 2-32,2-33 2-2,2-3 2-8,2-9 2-2,2-3	Transfer Acknowledge
/IRQ_AV /IRQ_AV /IRQ_AV /IRQ_AV	IC1 IC16 R64 TP349	16 96 1 P\$1	2-20,2-21 2-12,2-13 2-20,2-21 2-12,2-13	Interrupt Avia	/TS /TS /TS /TS	IC17 IC5 RN9 TP376	51 66 5 P\$1	2-32,2-33 2-2,2-3 2-2,2-3 2-32,2-33	Transfer Start

SeleXX PDR 5000 S DIG Signale / Signale /

Signal	Bauteil Part	Pin	Seite Page	Beschreibung Description	Signal	Bauteil Part	Pin	Seite Page	Beschreibung Description
					A7	TP175	P\$1	2-2,2-3	
/UA_EN	IC15	1	2-32,2-33	Adress Enable	4.0	1000	00	0007	A 1 1.21
/UA_EN	IC15	19	2-32,2-33	Steuerleitung Bustreiber	A8	IC20	36	2-6,2-7	Adressleitung
/UA_EN	IC17		2-32,2-33		A8	IC5	14	2-2,2-3	
/UA_EN	IC20 IC20	1 24	2-6,2-7		A8	TP240	P\$1	2-2,2-3	
/UA_EN /UA_EN	R36	1	2-6,2-7 2-32,2-33		A9	IC20	35	2-6,2-7	Adressleitung
/UA_EN	TP369	P\$1			A9	IC5	15	2-2,2-3	Adiessieitung
/OA_LIN	11 303	ιψι	2-02,2-00		A9	TP241		2-2,2-3	
/VSYNC	IC1	158	2-20,2-21	Vertikale Synchr.	Að	11 241	ιψι	2-2,2-3	
/VSYNC	IC17	29	2-32,2-33	Video-Signal	A10	IC20	33	2-6,2-7	Adressleitung
/VSYNC	IC4	7	2-24,2-25	riado eigila.	A10	IC5	16	2-2,2-3	7.ta.000.0.ta.1g
/VSYNC	TP148	P\$1			A10	TP270	P\$1		
/VSYNC	TP514		2-32,2-33				. ψ.	,_ 0	
,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,			,		A11	IC20	32	2-6,2-7	Adressleitung
/V_REQ	IC1	191	2-20,2-21	Video Request	A11	IC5	18	2-2,2-3	ŭ
/V_REQ	IC16	55	2-12,2-13	·	A11	TP243	P\$1	2-2,2-3	
/V_REQ	IC5	202	2-2,2-3						
/V_REQ	R54	2	2-20,2-21		A12	IC20	30	2-6,2-7	Adressleitung
/V_REQ	TP368	P\$1	2-20,2-21		A12	IC5	19	2-2,2-3	_
					A12	TP244	P\$1	2-2,2-3	
/WRITE	IC16	48	2-12,2-13	Write					
/WRITE	IC17	30	2-32,2-33	Initialisierung Xilinx	A13	IC20	29	2-6,2-7	Adressleitung
/WRITE	TP324	P\$1	2-12,2-13		A13	IC5	20	2-2,2-3	
					A13	TP271	P\$1	2-2,2-3	
/WRST	IC14	5	2-8,2-9	Reset Signal					
/WRST	IC16	83	2-12,2-13		A14	IC20	27	2-6,2-7	Adressleitung
/WRST	IC17	131	2-32,2-33		A14	IC5	22	2-2,2-3	
/WRST	IC22	27	2-18,2-19		A14	TP246	P\$1	2-2,2-3	
/WRST	IC40	1	2-6,2-7						
/WRST	JP1	14	2-36		A15	IC20	26	2-6,2-7	Adressleitung
					A15	IC5	23	2-2,2-3	
22KHZ	IC24	6	2-30,2-31	22 kHz Umschaltsignal	A15	TP247	P\$1	2-2,2-3	
22KHZ	R141	2	2-30,2-31	Tuner					
					A16	IC16	82	2-12,2-13	Adressleitung
A/CS0	IC22	5	2-18,2-19	siehe /CS0IDE1	A16	IC17	95	2-32,2-33	
A/CS0	JP6	37	2-18,2-19	Treiber	A16	IC21	2	2-4,2-5	
					A16	IC27	23	2-4,2-5	
A/CS1	IC22	20	2-18,2-19	siehe /CS1IDE1	A16	IC5	24	2-2,2-3	
A/CS1	JP6	38	2-18,2-19	nach Treiber	A16	TP342	P\$1	2-4,2-5	
A/CS1	TP193	P\$1	2-18,2-19						
A // O O O 4 O	1000	40	0.40.0.40	-1-1- /100010	A17	IC16	80	2-12,2-13	Adressleitung
A/IOCS16	IC22	40	2-18,2-19	siehe /IOCS16	A17	IC17	96	2-32,2-33	
A/IOCS16	JP6	32	2-18,2-19	nach Treiber	A17	IC21	30	2-4,2-5	
A/IOCS16	RN1	2	2-18,2-19		A17	IC27 IC5	33	2-4,2-5	
A/IOR	IC22	3	2-18,2-19	siehe /IOR	A17 A17	TP341	26	2-2,2-3 2-4,2-5	
A/IOR A/IOR	JP6	3 25	2-16,2-19	nach Treiber	AII	17341	гфі	2-4,2-3	
A/IOR A/IOR	TP186		2-18,2-19	nach heiber	A18	IC16	79	2-12,2-13	Adressleitung
Alon	11 100	ΙΨΙ	2-10,2-19		A18	IC17	99	2-32,2-33	Adiessieitung
A/IOW	IC22	2	2-18,2-19	siehe /IOW	A18	IC27	34	2-4,2-5	
A/IOW	JP6	23	2-18,2-19	nach Treiber	A18	IC5	27	2-2,2-3	
A/IOW	TP185		2-18,2-19		A18	TP333		2-4,2-5	
			_ :=,_ :=					,	
A/RES	IC22	22	2-18,2-19	siehe /WRST	A19	IC17	100	2-32,2-33	Adressleitung
A/RES	JP6	1	2-18,2-19	nach Treiber	A19	IC27	22	2-4,2-5	ŭ
					A19	IC5	28	2-2,2-3	
A0	IC20	47	2-6,2-7	Adressleitung	A19	TP343	P\$1	2-4,2-5	
A0	IC5	2	2-2,2-3	-					
A0	TP232		2-2,2-3		A20	IC17	102	2-32,2-33	Adressleitung
					A20	IC27	35	2-4,2-5	
A1	IC20	46	2-6,2-7	Adressleitung	A20	IC5	30	2-2,2-3	
A1	IC5	3	2-2,2-3		A20	TP344	P\$1	2-4,2-5	
A1	TP235	P\$1	2-2,2-3			10:-			
	16				A21	IC17		2-32,2-33	Adressleitung
A2	IC20	44	2-6,2-7	Adressleitung	A21	IC27	21	2-4,2-5	
A2	IC5	5	2-2,2-3		A21	IC5	31	2-2,2-3	
A2	TP236	P\$1	2-2,2-3		A21	TP337	P\$1	2-4,2-5	
A 0	1000	40	0.607	Advocalaituna	400	1017	110	0.00.0.00	A dragalaitusa
A3	IC20	43	2-6,2-7	Adressleitung	A22	IC17		2-32,2-33	Adressleitung
A3	IC5	6 D¢1	2-2,2-3		A22	IC27 IC5	20	2-4,2-5	
A3	TP111	гֆΙ	2-2,2-3		A22	TP336	32 D¢1	2-2,2-3	
A4	IC20	41	2-6,2-7	Adressleitung	A22	117330	гфІ	2-4,2-5	
A4 A4	IC20 IC5	8	2-0,2-7 2-2,2-3	Adiessicituity	A23	IC17	112	2-32,2-33	Adressleitung
A4 A4	TP238		2-2,2-3 2-2,2-3		A23 A23	IC17	34	2-32,2-33 2-2,2-3	Auressieiturig
7.7	11 230	ιφί	L L,L-U		A23 A23	TP378		2-2,2-3	
A5	IC20	40	2-6,2-7	Adressleitung	,	0,0	. ψι	_ 0_,_ 00	
A5 A5	IC5	9	2-0,2-7	, all coolenaing	A24	IC15	12	2-32,2-33	Adressleitung
A5 A5	TP173		2-2,2-3		A24 A24	IC5	35	2-32,2-33	. tar obstaring
,		ιψι	,_ 0		A24 A24	TP226		2-2,2-3	
A6	IC20	38	2-6,2-7	Adressleitung			. Ψι	,	
A6	IC5	11	2-2,2-3	<b>3</b>	A25	IC15	14	2-32,2-33	Adressleitung
A6	TP239		2-2,2-3		A25	IC5	36	2-2,2-3	
		+.	<i>,</i> -		A25	TP225		2-2,2-3	
A7	IC20	37	2-6,2-7	Adressleitung					
A7	IC5	12	2-2,2-3	Č	A26	IC15	16	2-32,2-33	Adressleitung
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Signal	Bauteil Part	Pin	Seite Page	Beschreibung Description	Signal	Bauteil Part	Pin	Seite Page	Beschreibung Description
A26 A26	IC5 TP224	38 P\$1	2-2,2-3 2-2,2-3		BA1 BA1	IC8 RN21	38 2	2-16,2-17 2-6,2-7	
A27 A27	IC15 IC5	18 39	2-32,2-33 2-2,2-3	Adressleitung	BA1	TP375	P\$1	2-20,2-21	Adressleitung
A27 A28	TP223 IC15	P\$1 3	2-2,2-3 2-32,2-33	Adressleitung	BA2 BA2 BA2	IC17 IC21 IC22	66 10 36	2-32,2-33 2-4,2-5 2-18,2-19	
A28 A28	IC5 TP222	40 P\$1	2-2,2-3 2-2,2-3	-	BA2 BA2 BA2	IC8 RN21 TP374	39 3 P\$1	2-16,2-17 2-6,2-7 2-32,2-33	
A29 A29 A29	IC15 IC5 TP221	5 42 P\$1	2-32,2-33 2-2,2-3 2-2,2-3	Adressleitung	BA3 BA3	IC17 IC21	74 9	2-32,2-33 2-4,2-5	Adressleitung
A30	IC15	гф1 7	2-2,2-3	Adressleitung	BA3 BA3	IC22 IC8	33 40	2-4,2-5 2-18,2-19 2-16,2-17	
A30 A30	IC5 TP219	43 P\$1	2-2,2-3 2-2,2-3		BA3 BA3	RN21 TP373	4 P\$1	2-6,2-7 2-32,2-33	
A31 A31	IC15 IC5	9 44	2-32,2-33 2-2,2-3	Adressleitung	BA4 BA4	IC17 IC21	75 8	2-32,2-33 2-4,2-5	Adressleitung
A31	TP220		2-2,2-3		BA4 BA4	IC8 RN20	41 1	2-16,2-17 2-6,2-7	
AM AM AM	D18 D19 R49	1 1 1	2-26,2-27 2-26,2-27 2-26,2-27	Audio Mute Tonabschaltung	BA4 BA5	TP372 IC17	P\$1 76	2-32,2-33	Adressleitung
AM AM	T9 TP80	3 P\$1	2-26,2-27		BA5 BA5	IC21 IC8	7 42	2-4,2-5 2-16,2-17	ŭ
AM	TP82	P\$1	2-26,2-27		BA5 BA5	RN20 TP371	2 P\$1	2-6,2-7 2-32,2-33	
AUDIO_L AUDIO_L AUDIO_L	C132 C193 C197	2 2 2	2-28,2-29 2-28,2-29 2-28,2-29	Audio Ausgang links	BA6 BA6	IC17 IC21	77 6	2-32,2-33 2-4,2-5	Adressleitung
AUDIO_L AUDIO_L	R130 R139	2	2-28,2-29 2-28,2-29		BA6 BA6	IC8 RN20	43 3	2-16,2-17 2-6,2-7	
AUDIO_L AUDIO_L	R28 R29	2 1	2-26,2-27 2-26,2-27		BA6	TP370	P\$1	2-32,2-33	
AUDIO_L AUDIO_L	R94 T4	2	2-28,2-29 2-26,2-27		BA7 BA7	IC17 IC21	79 5	2-32,2-33 2-4,2-5	Adressleitung
AUDIO_L	TP380	P\$1	,	Audia Augustus vaaluks	BA7 BA7	IC8 RN20	44 4	2-16,2-17 2-6,2-7	
AUDIO_R AUDIO_R	C131 C191 C196	2 2 2	2-28,2-29 2-28,2-29	Audio Ausgang rechts	BA7 BA8	TP383	P\$1 80	2-32,2-33	Adropoloitung
AUDIO_R AUDIO_R	R101	2	2-28,2-29 2-28,2-29		BA8	IC21	27	2-4,2-5	Adressleitung
AUDIO_R AUDIO_R	R104 R27	1 2	2-28,2-29 2-26,2-27		BA8 BA8	IC8 RN19	45 1	2-16,2-17 2-6,2-7	
AUDIO_R AUDIO_R AUDIO_R	R55 R93 T8	1 2 3	2-26,2-27 2-28,2-29 2-26,2-27		BA8 BA9	TP384 IC16	P\$1 98	2-32,2-33 2-12,2-13	Adressleitung
AUDIO_R	TP379	P\$1			BA9 BA9	IC17 IC21	83 26	2-32,2-33 2-4,2-5	· ·
A_DA0 A_DA0	IC22 JP6	14 35	2-18,2-19 2-18,2-19	Adressleitung Festplatte	BA9 BA9	IC27 IC8	32 46	2-4,2-5 2-16,2-17	
A_DA0	TP191	P\$1	2-18,2-19	Advanta ikus sa Farabalana	BA9 BA9	RN19 TP385	2 P\$1	2-6,2-7	
A_DA1 A_DA1	JP6	13 33	2-18,2-19 2-18,2-19	Adressleitung Festplatte	BA10	IC16	97	2-12,2-13	Adressleitung
A_DA1	TP189	P\$1			BA10 BA10	IC17 IC21	84 23	2-32,2-33 2-4,2-5	
A_DA2 A_DA2	IC22 JP6	16 36	2-18,2-19 2-18,2-19	Adressleitung Festplatte	BA10 BA10	IC27 IC8	31 47	2-4,2-5 2-16,2-17	
A_DA2	TP190		2-18,2-19	sighs //OCUPDV	BA10 BA10	RN19 TP386	3 P\$1	2-6,2-7 2-32,2-33	
A_INTRQ A_INTRQ	JP6	30 31	2-18,2-19 2-18,2-19	siehe /IOCHRDY nach Treiber	BA11	IC17	85	2-32,2-33	Adressleitung
A_INTRQ	R7	2	2-18,2-19	sisks (IODD)	BA11 BA11	IC21 IC27	25 30	2-4,2-5 2-4,2-5	
A_IORDY A_IORDY A_IORDY	IC22 JP6 RN1	37 27 4	2-18,2-19 2-18,2-19 2-18,2-19	siehe /IORDY nach Treiber	BA11 BA11	RN19 TP387	4 P\$1		
A_MUTE	D21	2	2-26,2-27	Tonabschaltung	BA12 BA12	IC17 IC21	86 4	2-32,2-33 2-4,2-5	Adressleitung
A_MUTE A_MUTE	IC40 TP381	12 P\$1	2-6,2-7 2-26,2-27	Steuerleitung	BA12 BA12 BA12	IC27 RN18 TP388	29 1 P\$1	2-4,2-5 2-6,2-7 2-32,2-33	
BA0 BA0	IC1 IC16	99	2-20,2-21 2-12,2-13	Adressleitung	BA13	IC17	87	2-32,2-33	Adressleitung
BA0 BA0	IC17 IC21	63 12	2-32,2-33 2-4,2-5		BA13 BA13	IC21 IC27	28 26	2-4,2-5 2-4,2-5	
BA0 BA0	RN21 TP233	1 P\$1	2-6,2-7 2-20,2-21		BA13 BA13	RN18 TP389	2 P\$1	2-6,2-7 2-32,2-33	
BA1 BA1	IC1 IC17	203 65	2-20,2-21 2-32,2-33	Adressleitung	BA14 BA14	IC17 IC21	93 29	2-32,2-33 2-4,2-5	Adressleitung
BA1 BA1	IC21 IC22	11 35	2-4,2-5 2-18,2-19		BA14 BA14	IC27 RN18	25 3	2-4,2-5 2-6,2-7	
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Signal BA14	Bauteil Part TP390	Pin P\$1	Page	Beschreibung Description	<b>Signal</b>	Bauteil Part RN14	Pin 8	Seite Page 2-6,2-7	Beschreibung Description
			•		D7	TP76		2-6,2-7	
BA15 BA15 BA15 BA15	IC17 IC21 IC27 RN18	94 3 24 4	2-32,2-33 2-4,2-5 2-4,2-5 2-6,2-7	Adressleitung	D8 D8	IC5 TP253		2-2,2-3 2-2,2-3	Datenleitung
BA15	TP391	P\$1	2-32,2-33		D9 D9	IC5 TP252		2-2,2-3 2-2,2-3	Datenleitung
BCLK0 BCLK0 BCLK0	IC30 R48 TP394	1 1 P\$1	2-6,2-7 2-2,2-3 2-6,2-7	Busclock	D10 D10	IC5 TP251		2-2,2-3 2-2,2-3	Datenleitung
BCLK_L BCLK_L BCLK_L	IC16 IC17 TP393	84 37 P\$1	2-12,2-13 2-32,2-33 2-32,2-33	Clock Initialisierung Xilinx	D11 D11	IC5 TP250		2-2,2-3 2-2,2-3	Datenleitung
BKPT/TMS	IC5		2-8,2-9	Break Point	D12 D12	IC5 TP249		2-2,2-3 2-2,2-3	Datenleitung
BKPT/TMS BKPT/TMS	JP2 RN17	2 6	2-8,2-9 2-8,2-9	Coldfire Debug Port	D13 D13	IC5 TP248		2-2,2-3 2-2,2-3	Datenleitung
CARD_INT	IC16	57	2-12,2-13	Interrupt Kartenleser					
CARD_INT	JP1	8	2-36		D14 D14	IC5 TP245		2-2,2-3 2-2,2-3	Datenleitung
CFBS CFBS	IC43 IC43	3 5	2-28,2-29 2-28,2-29	Video FBAS	D15	IC5	127	2-2,2-3	Datenleitung
CFBS CFBS	L7 R33	2	2-24,2-25 2-24,2-25		D15	TP242		2-2,2-3	Datementing
CFBS	R61	2	2-24,2-25		D16	IC17	40	2-32,2-33	Datenleitung
CFBS	TP395	P\$1	2-24,2-25		D16 D16	IC41 IC5	36 126	2-18,2-19 2-2,2-3	
CFBS_1	L19	1	2-28,2-29	Video Ausgang 1	D16	IC8	27	2-16,2-17	
CFBS_1 CFBS_1	R67 TP397	1 P\$1	2-28,2-29 2-28,2-29	(Scart 1)	D16 D16	RN28 TP414	5 P\$1	2-4,2-5 2-32,2-33	
CFBS_2	L20	1	2-28,2-29	Video Ausgang 2	D17	IC17	41	2-32,2-33	Datenleitung
CFBS_2	R88	1	2-20,2-29	(Scart 2)	D17	IC41	35	2-18,2-19	Datementing
CFBS_2	TP398	P\$1	2-28,2-29	•	D17	IC5		2-2,2-3	
OFBC 2	140	4	0.00.0.00	Video Auggeng 2	D17	IC8	26	2-16,2-17	
CFBS_3 CFBS_3 CFBS_3	L12 R19 TP399	1 1 P\$1	2-28,2-29 2-28,2-29 2-28,2-29	Video Ausgang 3 (Chinch)	D17 D17	RN28 TP413	6 P\$1	2-4,2-5 2-32,2-33	
_					D18	IC17	43	2-32,2-33	Datenleitung
CHROMA	L18	1	2-28,2-29	S-Video Helligkeit	D18	IC41	33	2-18,2-19	
CHROMA CHROMA	L4 R105	2 2	2-24,2-25 2-24,2-25		D18 D18	IC5 IC8	25	2-2,2-3 2-16,2-17	
CHROMA	TP396		2-24,2-25		D18	RN28	7	2-4,2-5	
CLK 07M	101	177	0.00.0.01	Clask Signal Audio Midas	D18	TP412	P\$1	2-32,2-33	
CLK_27M CLK_27M	IC1 IC1	177 178	2-20,2-21 2-20,2-21	Clock-Signal Audio/Video	D19	IC17	47	2-32,2-33	Datenleitung
CLK_27M	IC10	5	2-26,2-27		D19	IC41	32	2-18,2-19	3
CLK_27M	IC4	4	2-24,2-25		D19	IC5		2-2,2-3	
CLK_27M CLK_27M	R66 TP392	1 P\$1	2-32,2-33 2-32,2-33		D19 D19	IC8 RN28	24 8	2-16,2-17 2-4,2-5	
OLIT_L/III	11 002	. ψ.	L 0L,L 00		D19	TP411		2-32,2-33	
D0	IC5		2-2,2-3	Datenleitung	500	10.1-			5
D0 D0	RN13 TP58	5 P\$1	2-6,2-7 2-6,2-7		D20 D20	IC17 IC41	48 30	2-32,2-33 2-18,2-19	Datenleitung
Во	11 30	ιψι	2 0,2 1		D20	IC5		2-2,2-3	
D1	IC5		2-2,2-3	Datenleitung	D20	IC8	21	2-16,2-17	
D1 D1	RN13 TP59	6 P\$1	2-6,2-7 2-6,2-7		D20 D20	RN29 TP410	5 P\$1	2-4,2-5 2-32,2-33	
D1_OUT D1_OUT	D12 IC7	2 8	2-30,2-31 2-10,2-11	DiSEqC-Signal	D21 D21	IC17 IC41	50 29	2-32,2-33 2-18,2-19	Datenleitung
D1_OUT	TP400		2-10,2-11		D21	IC5		2-10,2-19	
_					D21	IC8	20	2-16,2-17	
D2 D2	IC5 RN13	144 7	2-2,2-3 2-6,2-7	Datenleitung	D21 D21	RN29 TP409	6 P\$1	2-4,2-5 2-32,2-33	
D2	TP60		2-6,2-7						
D3	ICE	140	0000	Dataplaitung	D22 D22	IC17 IC41	27	2-32,2-33	Datenleitung
D3	IC5 RN13	8	2-2,2-3 2-6,2-7	Datenleitung	D22 D22	IC41	27 118	2-18,2-19 2-2,2-3	
D3	TP62		2-6,2-7		D22	IC8	19	2-16,2-17	
5.4	105	4.40	0000	B + 1 **	D22	RN29	7	2-4,2-5	
D4 D4 D4	IC5 RN14 TP63	5	2-2,2-3 2-6,2-7 2-6,2-7	Datenleitung	D22 D23	TP416 IC17		2-32,2-33 2-32,2-33	Datenleitung
דט	11-03	гфІ	2-0,2-1		D23 D23	IC17	28 26	2-32,2-33 2-18,2-19	Datenleitung
D5	IC5	140	2-2,2-3	Datenleitung	D23	IC5	116	2-2,2-3	
D5	RN14	6 D¢1	2-6,2-7		D23	IC8	18	2-16,2-17	
D5	TP64	P\$1	2-6,2-7		D23 D23	RN29 TP415	8 P\$1	2-4,2-5 2-32,2-33	
D6	IC5	139	2-2,2-3	Datenleitung			. ψι	,_ 00	
D6	RN14	7	2-6,2-7	-	D24	IC1		2-20,2-21	Datenleitung
D6	TP68	P\$1	2-6,2-7		D24 D24	IC1 IC16	2 3	2-20,2-21 2-12,2-13	
D7	IC5	138	2-2,2-3	Datenleitung	D24 D24	IC17	39	2-32,2-33	

Signal	Bauteil Part		Seite Page	Beschreibung Description	Signal	Bauteil Part	Pin	Seite Page	Beschreibung Description
D24 D24 D24 D24 D24	IC21 IC40 IC41 IC5 IC8	13 3 37 115 65	2-4,2-5 2-6,2-7 2-18,2-19 2-2,2-3		DA_BCK DA_BCK DA_BCK	IC1 IC10 TP417	167 17 P\$1	2-20,2-21 2-26,2-27 2-26,2-27	digital Audio Bit Clock
D24 D24	RN26 TP401	5 P\$1	ŕ		DA_DATA DA_DATA DA_DATA	IC1 IC10 TP254	161 18 P\$1	2-20,2-21 2-26,2-27 2-26,2-27	digital Audio Daten
D25 D25 D25 D25	IC1 IC1 IC16 IC17	182 3 5 44	2-20,2-21 2-20,2-21 2-12,2-13 2-32,2-33	Datenleitung	DA_LRCK DA_LRCK DA_LRCK	IC1 IC10 TP256	166 19 P\$1	2-20,2-21 2-26,2-27	digital Audio R/L Clock
D25 D25 D25 D25	IC21 IC40 IC41 IC5	14 4 38 114	, -		DA_XCK DA_XCK DA_XCK	IC1 IC10 TP255	169 21 P\$1	2-20,2-21 2-26,2-27 2-20,2-21	digital Audio external Clock vom Audio Encoder
D25 D25 D25	IC8 RN26 TP402	66 6 P\$1	ŕ		DDATA0 DDATA0 DDATA0	IC5 JP2 TP203	186 19 P\$1	2-8,2-9 2-8,2-9 2-8,2-9	Debug Daten Coldfire
D26 D26 D26 D26	IC1 IC1 IC16 IC17	184 4 6 46	2-20,2-21 2-20,2-21 2-12,2-13 2-32,2-33	Datenleitung	DDATA1 DDATA1 DDATA1	IC5 JP2 TP206	187 18 P\$1	2-8,2-9 2-8,2-9 2-8,2-9	Debug Daten Coldfire
D26 D26 D26 D26	IC21 IC40 IC41 IC5		2-4,2-5 2-6,2-7 2-18,2-19 2-2,2-3		DDATA2 DDATA2 DDATA2	IC5 JP2 TP202	189 17 P\$1	2-8,2-9 2-8,2-9 2-8,2-9	Debug Daten Coldfire
D26 D26 D26	IC8 RN26 TP403	67 7 P\$1	2-16,2-17 2-4,2-5 2-32,2-33		DDATA3 DDATA3 DDATA3	IC5 JP2 TP207	190 16 P\$1	2-8,2-9 2-8,2-9 2-8,2-9	Debug Daten Coldfire
D27 D27 D27 D27 D27	IC1 IC1 IC16 IC17 IC21	185 6 7 49 17	2-20,2-21 2-20,2-21 2-12,2-13 2-32,2-33 2-4,2-5	Datenleitung	DONE DONE DONE DONE	IC16 IC17 R109 TP326	51 72 1 P\$1	2-12,2-13 2-32,2-33 2-32,2-33 2-12,2-13	Fertigmeldung Initialisierung Xilinx
D27 D27 D27 D27 D27	IC40 IC41 IC5 IC8 RN26	8 41 111 68 8	2-6,2-7 2-18,2-19 2-2,2-3 2-16,2-17 2-4,2-5		DRAMRW DRAMRW DRAMRW	IC27 RN7 TP447	16 5 P\$1	2-4,2-5 2-4,2-5 2-4,2-5	Read/Write SPEICHER
D27 D27 D28 D28	TP404 IC1 IC1		2-32,2-33 2-20,2-21 2-20,2-21	Datenleitung	DVB1_POL DVB1_POL DVB1_POL DVB1_POL	D14 D8 IC7 TP452	1 1 17 P\$1	2-34,2-35 2-30,2-31 2-10,2-11 2-30,2-31	Polarität Tuner (14/18V) Steuerleitung
D28 D28 D28 D28	IC17 IC21 IC40 IC41	57 18 13 43	2-32,2-33 2-4,2-5 2-6,2-7 2-18,2-19		DVB2_POL DVB2_POL	IC7 TP9	18	2-10,2-11 2-10,2-11	nicht verwendet ursprünglich für 2. Tuner
D28 D28 D28 D28	IC5 IC8 RN27 TP405	71 5	2-2,2-3 2-16,2-17 2-4,2-5 2-32,2-33		DVCC DVCC DVCC DVCC DVCC	C39 C40 C46 C47 CON7	2 2 1 2 9	2-10,2-11 2-10,2-11 2-10,2-11 2-10,2-11 2-36	Versorgungsspannung PIC/RTC
D29 D29 D29 D29 D29 D29 D29 D29 D29 D29	IC1 IC1 IC17 IC21 IC40 IC41 IC5 IC8 RN27 TP406	9 60 19 14 44 108 72 6	2-20,2-21 2-20,2-21 2-32,2-33 2-4,2-5 2-6,2-7 2-18,2-19 2-2,2-3 2-16,2-17 2-4,2-5 2-32,2-33	Datenleitung	DVCC DVCC DVCC DVCC DVCC DVCC DVCC DVCC	D3 IC7 JP13 R125 R126 R127 R40 R63 R76	1 14 3 2 2 2 1 2	2-10,2-11 2-10,2-11 2-10,2-11 2-10,2-11 2-10,2-11 2-10,2-11 2-10,2-11 2-10,2-11 2-10,2-11	
D30 D30 D30	IC1 IC1 IC1 IC17	10	2-20,2-21 2-20,2-21 2-32,2-33	Datenleitung	D_INT D_INT D_INT	IC40 IC7 TP234	5 6 P\$1	2-6,2-7 über I2C-Bus 2-6,2-7	Daten von Coldfire
D30 D30 D30 D30 D30 D30 D30	IC17 IC21 IC40 IC41 IC5 IC8 RN27	20 17 46	2-32,2-33 2-4,2-5 2-6,2-7 2-18,2-19 2-2,2-3 2-16,2-17 2-4,2-5		EDGE_SEL EDGE_SEL EDGE_SEL EDGE_SEL EDGE_SEL	IC5 IC5 R14 R48 TP449	170 2 2	2-4,2-5 2-2,2-3 2-4,2-5 2-2,2-3 2-2,2-3	Timing Select Übernahme der Daten
D30 D31 D31	TP407	P\$1 11	2-32,2-33 2-20,2-21 2-20,2-21	Datenleitung	ETH_IRQ ETH_IRQ ETH_IRQ ETH_IRQ	IC16 IC8 R117 TP346	20 35 2 P\$1	2-12,2-13 2-16,2-17 2-16,2-17 2-12,2-13	Interrupt Ethernet
D31 D31 D31 D31 D31	IC17 IC21 IC40 IC41 IC5	67 21 18 47 106	2-32,2-33 2-4,2-5 2-6,2-7 2-18,2-19 2-2,2-3		F_SELECT F_SELECT F_SELECT F_SELECT	CON6 CON6 R116 R121	29 8 2 2	2-28,2-29 2-28,2-29 2-10,2-11 2-10,2-11	Function Select Scart
D31 D31 D31	IC8 RN27 TP408	74 8 P\$1	2-16,2-17 2-4,2-5 2-32,2-33		GND GND	C1 C10	1	2-30,2-31 2-22,2-23	Masse

Signal	Bauteil Part	Pin	Seite Page	Beschreibung Description	Signal	Bauteil Part	Pin	Seite Page	Beschreibung Description
GND	C100	1	2-16,2-17		GND	C197	1	2-28,2-29	
GND	C102	1	2-12,2-13		GND	C198	1	2-28,2-29	
GND GND	C103 C104	1 1	2-16,2-17 2-12,2-13		GND GND	C20 C21	1 1	2-22,2-23 2-2,2-3	
GND	C104	1	2-12,2-13		GND	C214	1	2-2,2-3 2-28,2-29	
GND	C109	1	2-8,2-9		GND	C22	1	2-2,2-3	
GND	C11	1	2-8,2-9		GND	C225	2	2-34,2-35	
GND	C110	1	2-18,2-19		GND	C227	2	2-34,2-35	
GND	C111	1 2	2-18,2-19		GND	C23	1 2	2-2,2-3	
GND GND	C112 C113	2	2-36 2-36		GND GND	C230 C24	1	2-34,2-35 2-2,2-3	
GND	C114	2	2-36		GND	C25	1	2-2,2-3	
GND	C115	1	2-20,2-21		GND	C253	2	2-34,2-35	
GND	C116	2	2-30,2-31		GND	C254	2	2-34,2-35	
GND	C117	1	2-32,2-33		GND	C26	1	2-2,2-3	
GND	C118	1	2-4,2-5		GND	C27	1	2-2,2-3	
GND GND	C119 C12	1 1	2-34,2-35 2-22,2-23		GND GND	C28 C29	1 1	2-2,2-3 2-2,2-3	
GND	C120	2	2-24,2-25		GND	C3	1	2-26,2-27	
GND	C121	2	2-2,2-3		GND	C30	1	2-24,2-25	
GND	C122	2	2-4,2-5		GND	C31	1	2-24,2-25	
GND	C123	1	2-34,2-35		GND	C32	1	2-24,2-25	
GND	C124	1	2-6,2-7		GND	C33	1	2-26,2-27	
GND GND	C125 C126	1 2	2-26,2-27 2-16,2-17		GND GND	C34 C35	2 2	2-26,2-27 2-30,2-31	
GND	C120	1	2-10,2-17		GND	C36	1	2-30,2-31	
GND	C128	1	2-14,2-15		GND	C37	i	2-10,2-11	
GND	C129	1	2-20,2-21		GND	C38	1	2-12,2-13	
GND	C13	1	2-22,2-23		GND	C39	1	2-10,2-11	
GND	C130	2	2-32,2-33		GND	C4	1	2-26,2-27	
GND	C131	1 1	2-28,2-29		GND GND	C40	1 1	2-10,2-11	
GND GND	C132 C133	1	2-28,2-29 2-18,2-19		GND	C41 C42	1	2-24,2-25 2-24,2-25	
GND	C134	2	2-16,2-17		GND	C43	1	2-24,2-25	
GND	C135	2	2-16,2-17		GND	C44	1	2-24,2-25	
GND	C136	1	2-6,2-7		GND	C45	1	2-16,2-17	
GND	C137	1	2-30,2-31		GND	C46	2	2-10,2-11	
GND GND	C138 C139	1 1	2-30,2-31 2-30,2-31		GND GND	C47 C48	1 1	2-10,2-11 2-26,2-27	
GND	C139	1	2-30,2-31		GND	C48	1	2-20,2-27	
GND	C140	2	2-8,2-9		GND	C5	2	2-26,2-27	
GND	C141	2	2-8,2-9		GND	C50	1	2-26,2-27	
GND	C142	1	2-24,2-25		GND	C51	1	2-26,2-27	
GND	C143	1	2-24,2-25		GND	C52	2	2-26,2-27	
GND GND	C144 C145	1 1	2-24,2-25 2-24,2-25		GND GND	C53 C54	2 1	2-26,2-27 2-2,2-3	
GND	C145	1	2-24,2-25		GND	C55	1	2-2,2-3 2-16,2-17	
GND	C147	1	2-24,2-25		GND	C56	1	2-30,2-31	
GND	C15	1	2-22,2-23		GND	C57	1	2-16,2-17	
GND	C155	2	2-34,2-35		GND	C58	1	2-18,2-19	
GND	C156	1	2-32,2-33		GND	C59	2	2-20,2-21	
GND GND	C159 C16	1	2-20,2-21 2-22,2-23		GND GND	C6 C61	2 1	2-22,2-23 2-16,2-17	
GND	C160	1 1	2-22,2-23 2-26,2-27		GND	C62	1	2-16,2-17	
GND	C161	1	2-34,2-35		GND	C65	1	2-20,2-21	
GND	C163	2	2-26,2-27		GND	C66	1	2-18,2-19	
GND	C164	1	2-10,2-11		GND	C67	1	2-20,2-21	
GND	C166	1	2-10,2-11		GND	C68	1	2-20,2-21	
GND GND	C167 C168	2 1	2-2,2-3 2-10,2-11		GND GND	C69 C7	1 2	2-20,2-21 2-20,2-21	
GND	C169	2	2-10,2-11		GND	C70	1	2-20,2-21	
GND	C17	1	2-22,2-23		GND	C71	i	2-20,2-21	
GND	C170	1	2-32,2-33		GND	C72	1	2-20,2-21	
GND	C171	1	2-32,2-33		GND	C73	1	2-20,2-21	
GND	C172	1	2-30,2-31		GND	C74	1	2-20,2-21	
GND	C173	1	2-32,2-33		GND	C75	1	2-20,2-21	
GND GND	C174 C175	1 1	2-32,2-33 2-32,2-33		GND GND	C76 C77	1 1	2-20,2-21 2-2,2-3	
GND	C176	1	2-34,2-35		GND	C78	1	2-16,2-17	
GND	C177	1	2-20,2-21		GND	C79	2	2-16,2-17	
GND	C18	2	2-24,2-25		GND	C8	1	2-20,2-21	
GND	C181	2	2-20,2-21		GND	C80	1	2-6,2-7	
GND	C182	1	2-20,2-21		GND	C81	1	2-6,2-7	
GND GND	C183 C184	1 2	2-20,2-21 2-30,2-31		GND GND	C82 C83	1 1	2-6,2-7 2-6,2-7	
GND	C185	1	2-30,2-31		GND	C83	2	2-6,2-7 2-30,2-31	
GND	C186	1	2-34,2-35		GND	C85	1	2-18,2-19	
GND	C188	1	2-34,2-35		GND	C86	2	2-34,2-35	
GND	C189	1	2-34,2-35		GND	C87	2	2-34,2-35	
GND	C19	1	2-28,2-29		GND	C88	2	2-34,2-35	
GND	C190	1	2-28,2-29		GND	C89	1	2-8,2-9	
GND	C191	1	2-28,2-29		GND	C9	1	2-22,2-23	
GND GND	C192 C193	2 1	2-10,2-11 2-28,2-29		GND GND	C90 C91	1 1	2-30,2-31 2-8,2-9	
GND	C193	1	2-26,2-29		GND	C92	2	2-0,2-9	
GND	C195	1	2-28,2-29		GND	C93	1	2-4,2-5	
GND	C196	1	2-28,2-29		GND	C94	1	2-4,2-5	

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Signal	Bauteil Part	Pin	Seite Page	Beschreibung Description	Signal	Bauteil Part	Pin Seite Page
GND	C95	1	2-4,2-5	·	GND	IC15	15 2-32,2-33
GND	C96	1	2-4,2-5		GND	IC15	17 2-32,2-33
GND	C97	1	2-4,2-5		GND	IC15	2 2-32,2-33
GND	C98	1	2-4,2-5		GND	IC15	4 2-32,2-33
GND	C99	1	2-26,2-27		GND	IC15	6 2-32,2-33
GND GND	CON1 CON2	2 2	2-28,2-29 2-28,2-29		GND GND	IC15 IC16	8 2-32,2-33 14 2-12,2-13
GND	CON3	P\$1			GND	IC16	39 2-12,2-13
GND	CON3	P\$2	,		GND	IC16	61 2-12,2-13
GND	CON4	2	2-28,2-29		GND	IC16	86 2-12,2-13
GND	CON5	1	2-28,2-29		GND	IC17	110 2-32,2-33
GND GND	CON5 CON5	2 S1	2-28,2-29 2-28,2-29		GND GND	IC17 IC17	119 2-32,2-33 128 2-32,2-33
GND	CON5	S2	2-28,2-29		GND	IC17	135 2-32,2-33
GND	CON5	S3	2-28,2-29		GND	IC17	143 2-32,2-33
GND	CON6	13	2-28,2-29		GND	IC17	17 2-32,2-33
GND GND	CON6 CON6	14 17	2-28,2-29 2-28,2-29		GND GND	IC17 IC17	25 2-32,2-33 33 2-32,2-33
GND	CON6	18	2-28,2-29		GND	IC17	45 2-32,2-33
GND	CON6	21	2-28,2-29		GND	IC17	52 2-32,2-33
GND	CON6	25	2-28,2-29		GND	IC17	61 2-32,2-33
GND GND	CON6 CON6	26 30	2-28,2-29 2-28,2-29		GND GND	IC17 IC17	73 2-32,2-33 8 2-32,2-33
GND	CON6	34	2-28,2-29		GND	IC17	81 2-32,2-33
GND	CON6	35	2-28,2-29		GND	IC17	89 2-32,2-33
GND	CON6	38	2-28,2-29		GND	IC17	98 2-32,2-33
GND	CON6	39 4	2-28,2-29		GND	IC18	15 2-14,2-15
GND GND	CON6 CON6	4 42	2-28,2-29 2-28,2-29		GND GND	IC19 IC19	4 2-34,2-35 5 2-34,2-35
GND	CON6	5	2-28,2-29		GND	IC19	7 2-34,2-35
GND	CON6	9	2-28,2-29		GND	IC2	10 2-22,2-23
GND	CON7	13	2-36		GND	IC2	26 2-22,2-23
GND GND	CON7 CON7	15 16	2-36 2-36		GND GND	IC2 IC2	4 2-22,2-23 41 2-22,2-23
GND	CON7	17	2-36		GND	IC2	47 2-22,2-23
GND	CON7	3	2-36		GND	IC2	50 2-22,2-23
GND	CON7	5	2-36		GND	IC20	10 2-6,2-7
GND	CON7	7	2-36		GND	IC20	15 2-6,2-7
GND GND	CON8 CON8	10 11	2-14,2-15 2-14,2-15		GND GND	IC20 IC20	21 2-6,2-7 28 2-6,2-7
GND	CON8	5	2-14,2-15		GND	IC20	34 2-6,2-7
GND	IC1	103			GND	IC20	39 2-6,2-7
GND	IC1	109	2-20,2-21		GND	IC20	4 2-6,2-7
GND GND	IC1 IC1	115 119	2-20,2-21 2-20,2-21		GND GND	IC20 IC21	45 2-6,2-7 16 2-4,2-5
GND	IC1	125			GND	IC22	10 2-4,2-5
GND	IC1	136	2-20,2-21		GND	IC22	15 2-18,2-19
GND	IC1	14	2-20,2-21		GND	IC22	21 2-18,2-19
GND GND	IC1 IC1	146 151			GND GND	IC22 IC22	26 2-18,2-19 28 2-18,2-19
GND	IC1		2-20,2-21		GND	IC22	32 2-18,2-19
GND	IC1		2-20,2-21		GND	IC22	34 2-18,2-19
GND	IC1		2-20,2-21		GND	IC22	38 2-18,2-19
GND GND	IC1 IC1	183 19	2-20,2-21 2-20,2-21		GND GND	IC22 IC22	39 2-18,2-19 4 2-18,2-19
GND	IC1		2-20,2-21		GND	IC22	41 2-18,2-19
GND	IC1		2-20,2-21		GND	IC22	43 2-18,2-19
GND	IC1	29	2-20,2-21		GND	IC22	45 2-18,2-19
GND GND	IC1 IC1	38 42	2-20,2-21 2-20,2-21		GND GND	IC23 IC23	2 2-34,2-35 3 2-34,2-35
GND	IC1	49	2-20,2-21		GND	IC23	6 2-34,2-35
GND	IC1	57	2-20,2-21		GND	IC23	7 2-34,2-35
GND	IC1	63	2-20,2-21		GND	IC24	35 2-30,2-31
GND	IC1	67	2-20,2-21		GND	IC24	36 2-30,2-31
GND GND	IC1 IC1	7 71	2-20,2-21 2-20,2-21		GND GND	IC24 IC24	37 2-30,2-31 38 2-30,2-31
GND	IC1	77	2-20,2-21		GND	IC24	9 2-30,2-31
GND	IC1	83	2-20,2-21		GND	IC25	1 2-30,2-31
GND	IC1	89	2-20,2-21		GND	IC25	16 2-30,2-31
GND GND	IC1 IC1	93 97	2-20,2-21 2-20,2-21		GND GND	IC25 IC25	31 2-30,2-31 32 2-30,2-31
GND	IC10	12	2-26,2-27		GND	IC25	33 2-30,2-31
GND	IC10	2	2-26,2-27		GND	IC25	34 2-30,2-31
GND	IC10	23	2-26,2-27		GND	IC25	4 2-30,2-31
GND	IC10	6	2-26,2-27		GND	IC25	7 2-30,2-31
GND GND	IC10 IC12	7 2	2-26,2-27 2-26,2-27		GND GND	IC25 IC27	9 2-30,2-31 12 2-4,2-5
GND	IC12	3	2-26,2-27		GND	IC27	28 2-4,2-5
GND	IC12	6	2-26,2-27		GND	IC27	41 2-4,2-5
GND	IC12	7	2-26,2-27		GND	IC27	46 2-4,2-5
GND GND	IC13 IC13	1 4	2-8,2-9 2-8,2-9		GND GND	IC27 IC27	52 2-4,2-5 54 2-4,2-5
GND	IC13	1	2-8,2-9		GND	IC27	6 2-4,2-5
GND	IC14	4	2-8,2-9		GND	IC28	15 2-14,2-15
GND	IC15	10	2-32,2-33		GND	IC29	2 2-30,2-31
GND GND	IC15 IC15	11 13	2-32,2-33 2-32,2-33		GND GND	IC29 IC29	3 2-30,2-31 6 2-30,2-31
J	.5.15	.5	_ 5_,_ 55		GI1D	.020	2 200,201

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Signal	Bauteil Part	Pin	Seite Page	Beschreibung Description	Signal	Bauteil Part	Pin	Seite Page	Beschreibung Description
GND	IC29	7	2-30,2-31		GND	JP6	2	2-18,2-19	
GND	IC3	5	2-20,2-21		GND	JP6	22	2-18,2-19	
GND	IC30	4	2-6,2-7		GND	JP6	24	2-18,2-19	
GND	IC31	4	2-10,2-11		GND	JP6	26	2-18,2-19	
GND	IC37	4	2-34,2-35		GND	JP6	30	2-18,2-19	
GND	IC4	18	2-24,2-25		GND	JP6	40	2-18,2-19	
GND	IC4	32	2-24,2-25		GND	JP7	2	2-10,2-11	
GND GND	IC4 IC4	33 38	2-24,2-25		GND GND	L10	3 3	2-28,2-29	
GND	IC4	5	2-24,2-25 2-24,2-25		GND	L12 L17	3	2-28,2-29 2-28,2-29	
GND	IC40	10	2-6,2-7		GND	L18	3	2-28,2-29	
GND	IC41	10	2-18,2-19		GND	L19	3	2-28,2-29	
GND	IC41	15	2-18,2-19		GND	L20	3	2-28,2-29	
GND	IC41	21	2-18,2-19		GND	L21	3	2-28,2-29	
GND	IC41	28	2-18,2-19		GND	L22	3	2-28,2-29	
GND	IC41	34	2-18,2-19		GND	L23	3	2-28,2-29	
GND	IC41	39	2-18,2-19		GND	L24	3	2-28,2-29	
GND GND	IC41 IC41	4 45	2-18,2-19 2-18,2-19		GND GND	L6 Q6	3 2	2-28,2-29 2-2,2-3	
GND	IC41	45	2-16,2-19		GND	R102	1	2-2,2-3 2-20,2-21	
GND	IC43	4	2-28,2-29		GND	R106	1	2-32,2-33	
GND	IC5	10	2-2,2-3		GND	R114	1	2-16,2-17	
GND	IC5	101	2-2,2-3		GND	R117	1	2-16,2-17	
GND	IC5	104	2-2,2-3		GND	R120	2	2-16,2-17	
GND	IC5		2-2,2-3		GND	R123	1	2-26,2-27	
GND	IC5	117	,		GND	R128	1	2-10,2-11	
GND	IC5		2-2,2-3		GND	R129	2	2-34,2-35	
GND	IC5		2-2,2-3		GND	R136	1	2-4,2-5	
GND GND	IC5 IC5	141	2-2,2-3 2-2,2-3		GND GND	R15 R20	1 1	2-8,2-9 2-34,2-35	
GND	IC5		2-2,2-3		GND	R27	1	2-26,2-27	
GND	IC5		2-2,2-3		GND	R28	i	2-26,2-27	
GND	IC5	169	2-2,2-3		GND	R3	1	2-16,2-17	
GND	IC5	17	2-2,2-3		GND	R33	2	2-24,2-25	
GND	IC5	173	2-2,2-3		GND	R35	2	2-28,2-29	
GND	IC5		2-2,2-3		GND	R39	1	2-34,2-35	
GND	IC5		2-2,2-3		GND	R43	2	2-26,2-27	
GND	IC5		2-2,2-3		GND	R57	1	2-6,2-7	
GND	IC5		2-2,2-3		GND	R58	2	2-28,2-29	
GND GND	IC5 IC5	201 208	2-2,2-3 2-2,2-3		GND GND	R68 R69	1 1	2-16,2-17 2-20,2-21	
GND	IC5	25	2-2,2-3 2-2,2-3		GND	R7	1	2-20,2-21	
GND	IC5	33	2-2,2-3		GND	R71	i	2-24,2-25	
GND	IC5	4	2-2,2-3		GND	R73	2	2-24,2-25	
GND	IC5	41	2-2,2-3		GND	R74	1	2-36	
GND	IC5	48	2-2,2-3		GND	R81	1	2-10,2-11	
GND	IC5	53	2-2,2-3		GND	R87	1	2-34,2-35	
GND	IC5	61	2-2,2-3		GND	R89	1	2-18,2-19	
GND	IC5	69 77	2-2,2-3		GND	R9	1 1	2-8,2-9	
GND GND	IC5 IC5	77 85	2-2,2-3 2-2,2-3		GND GND	R91 R92	2	2-18,2-19 2-34,2-35	
GND	IC5	93	2-2,2-3		GND	R96	1	2-30,2-31	
GND	IC6	1	2-26,2-27		GND	R98	1	2-36	
GND	IC7	5	2-10,2-11		GND	RN10	1	2-2,2-3	
GND	IC8	1	2-16,2-17		GND	RN10	2	2-2,2-3	
GND	IC8	10	2-16,2-17		GND	RN10	3	2-2,2-3	
GND	IC8	23	2-16,2-17		GND	RN10	4	2-2,2-3	
GND	IC8	55	2-16,2-17		GND	RN13	2	2-6,2-7	
GND GND	IC8 IC8	57 70	2-16,2-17 2-16,2-17		GND GND	RN13 RN14	4 2	2-6,2-7 2-6,2-7	
GND	IC8	8	2-16,2-17		GND	RN14	4	2-6,2-7	
GND	IC8	86	2-16,2-17		GND	RN16	7	2-2,2-3	
GND	IC8	89	2-16,2-17		GND	RN30	2	2-20,2-21	
GND	IC8	94	2-16,2-17		GND	RN30	3	2-20,2-21	
GND	IC8	96	2-16,2-17		GND	S1	2	2-8,2-9	
GND	IC9	10	2-22,2-23		GND	S1	3	2-8,2-9	
GND	IC9	26	2-22,2-23		GND	S1	4	2-8,2-9	
GND	IC9	4	2-22,2-23		GND	T1	1	2-34,2-35	
GND GND	IC9 IC9	41 47	2-22,2-23 2-22,2-23		GND GND	T1 T1	2	2-34,2-35	
GND	IC9	50	2-22,2-23		GND	T12	3 1	2-34,2-35 2-10,2-11	
GND	JP1	11	2-22,2-23		GND	T13	1	2-10,2-11	
GND	JP1	13	2-36		GND	T14	1	2-10,2-11	
GND	JP1	3	2-36		GND	T2	1	2-10,2-11	
GND	JP13	10	2-10,2-11		GND	T3	1	2-30,2-31	
GND	JP13	2	2-10,2-11		GND	T4	1	2-26,2-27	
GND	JP2	11	2-8,2-9		GND	T6	1	2-30,2-31	
GND	JP2	20	2-8,2-9		GND	T7	1	2-30,2-31	
GND	JP2	23	2-8,2-9		GND	T8	1 1	2-26,2-27	
GND GND	JP2 JP2	3 5	2-8,2-9 2-8,2-9		GND	Т9	1	2-26,2-27	
GND	JP3	11	2-0,2-9 2-14,2-15		IEC958	IC1	159	2-20,2-21	digital Audio Ausgang
GND	JP3	13	2-14,2-15		IEC958	IC6	4	2-26,2-27	g
GND	JP3	15	2-14,2-15					,	
GND	JP3	9	2-14,2-15		IR-IN	IC7	7	2-10,2-11	Fernbedienung Eingang
GND	JP5	7	2-12,2-13		IR-IN	JP13	5	2-10,2-11	
GND	JP6	19	2-18,2-19						

Signal  IRDL IRDL IRDL IRDL IRDL	Bauteil Part IC7 R127 T14 TP450	Pin 9 1 3 P\$1	Seite Page 2-10,2-11 2-10,2-11 2-10,2-11 2-10,2-11	Beschreibung Description IR Downlink über Scart	Signal MA9 MA9 MA9 MA9 MA9	Bauteil Part IC1 IC2 IC9 TP428	Pin 96 32 32 P\$1	Seite Page 2-22,2-23 2-22,2-23 2-22,2-23 2-22,2-23	Beschreibung Description Speicheradresse Avia
IRQ_IDE1 IRQ_IDE1 IRQ_IDE1	IC16 IC22 TP352	73 19 P\$1	2-12,2-13 2-18,2-19 2-12,2-13	Interrupt Festplatte	MA10 MA10 MA10 MA10	IC1 IC2 IC9 TP429	100 20 20 P\$1	2-22,2-23 2-22,2-23 2-22,2-23 2-22,2-23	Speicheradresse Avia
L L L	CON2 L10 TP78	1 2 P\$1	2-28,2-29 2-28,2-29 2-28,2-29	Audio Ausgang Links	MA11 MA11 MA11	IC1 IC2 IC9	98 19 19	2-22,2-23 2-22,2-23 2-22,2-23	Speicheradresse Avia
LDQM LDQM LDQM LDQM	IC1 IC2 IC9 TP418	79 14 14 P\$1	2-22,2-23 2-22,2-23 2-22,2-23 2-22,2-23	Data I/O Mask Avia SPEICHER	MA11  MBUS+5V  MBUS+5V	TP430 C189 IC23	2	2-22,2-23 2-34,2-35 2-34,2-35	Pullup Spannung MBUS
LNBA LNBA LNBA LNBA	C36 D11 D22 IC24	2 2 2 5	2-30,2-31 2-30,2-31 2-30,2-31 2-30,2-31	Tuner Ausgang	MBUS+5V MBUS+5V MCF_LED1 MCF_LED1	IC25 RN2 IC40 JP13	15 1 16 4	2-30,2-31 2-34,2-35 2-6,2-7 2-10,2-11	LED Grün Coldfire
LNB_LOW LNB_LOW LNB_LOW	D25 D4 R131	1 1 2	2-34,2-35 2-34,2-35 2-34,2-35	Steuerspannung 14/18V Tuner	MCF_LED2 MCF_LED2	IC40 JP13	19 6	2-6,2-7 2-10,2-11	LED Rot Coldfire
LNB_LOW  LNB_SC LNB_SC	TP453 D7 D9		2-34,2-35 2-34,2-35 2-30,2-31	Kurzschluss Tuner erkannt	MD0 MD0 MD0 MD0	IC1 IC2 IC9 TP431	54 2 2 P\$1	2-22,2-23 2-22,2-23 2-22,2-23 2-22,2-23	Daten Avia Speicher
LNB_SC LNB_SC LNB_SC LNB_SC	IC16 R95 T7 TP451	68 2 3 P\$1	2-12,2-13 2-30,2-31 2-30,2-31 2-30,2-31		MD1 MD1 MD1	IC1 IC2 IC9	58 3 3	2-22,2-23 2-22,2-23 2-22,2-23	Daten Avia Speicher
L_BCLK L_BCLK L_BCLK	IC16 R134 TP542	10 2 P\$1	2-12,2-13 2-6,2-7 2-6,2-7	Busclock	MD1 MD2 MD2 MD2	TP432 IC1 IC2 IC9	P\$1 60 5 5	2-22,2-23 2-22,2-23 2-22,2-23 2-22,2-23	Daten Avia Speicher
L_NC1 L_NC1	IC16 TP41	85 P\$1	2-12,2-13 2-12,2-13	nicht verwendet	MD2	TP433	P\$1	2-22,2-23	Datan Avia Chaichar
MAO MAO MAO MAO	IC1 IC2 IC9 TP419	21 21	2-22,2-23 2-22,2-23 2-22,2-23 2-22,2-23	Speicheradresse Avia	MD3 MD3 MD3 MD3	IC1 IC2 IC9 TP434	64 6 6 P\$1	2-22,2-23 2-22,2-23 2-22,2-23 2-22,2-23	Daten Avia Speicher
MA1 MA1 MA1 MA1	IC1 IC2 IC9 TP420	22 22	2-22,2-23 2-22,2-23 2-22,2-23 2-22,2-23	Speicheradresse Avia	MD4 MD4 MD4 MD4	IC1 IC2 IC9 TP435	68 8 8 P\$1	2-22,2-23 2-22,2-23 2-22,2-23 2-22,2-23	Daten Avia Speicher
MA2 MA2 MA2	IC1 IC2 IC9	110 23 23	2-22,2-23 2-22,2-23 2-22,2-23	Speicheradresse Avia	MD5 MD5 MD5 MD5	IC1 IC2 IC9 TP436	72 9 9 P\$1	2-22,2-23 2-22,2-23 2-22,2-23 2-22,2-23	Daten Avia Speicher
MA2 MA3 MA3 MA3 MA3	TP421 IC1 IC2 IC9 TP422	112 24 24	2-22,2-23 2-22,2-23 2-22,2-23 2-22,2-23 2-22,2-23	Speicheradresse Avia	MD6 MD6 MD6 MD6	IC1 IC2 IC9 TP437	74 11 11 P\$1	2-22,2-23 2-22,2-23 2-22,2-23 2-22,2-23	Daten Avia Speicher
MA4 MA4 MA4 MA4	IC1 IC2 IC9 TP423	111 27 27	2-22,2-23 2-22,2-23 2-22,2-23 2-22,2-23	Speicheradresse Avia	MD7 MD7 MD7 MD7	IC1 IC2 IC9 TP438	78 12 12 P\$1	2-22,2-23 2-22,2-23 2-22,2-23 2-22,2-23	Daten Avia Speicher
MA5 MA5 MA5 MA5	IC1 IC2 IC9 TP424	108 28 28	2-22,2-23 2-22,2-23 2-22,2-23 2-22,2-23	Speicheradresse Avia	MD8 MD8 MD8 MD8	IC1 IC2 IC9 TP439	76 39 39 P\$1	2-22,2-23 2-22,2-23 2-22,2-23 2-22,2-23	Daten Avia Speicher
MA6 MA6 MA6 MA6	IC1 IC2 IC9 TP425	105 29 29	2-22,2-23 2-22,2-23 2-22,2-23 2-22,2-23	Speicheradresse Avia	MD9 MD9 MD9 MD9	IC1 IC2 IC9 TP440	73 40 40 P\$1	2-22,2-23 2-22,2-23 2-22,2-23 2-22,2-23	Daten Avia Speicher
MA7 MA7 MA7 MA7	IC1 IC2 IC9 TP426	102 30 30	2-22,2-23 2-22,2-23 2-22,2-23	Speicheradresse Avia	MD10 MD10 MD10 MD10	IC1 IC2 IC9 TP441	70 42 42 P\$1	2-22,2-23 2-22,2-23 2-22,2-23 2-22,2-23	Daten Avia Speicher
MA8 MA8 MA8	IC1 IC2 IC9	99 31 31	2-22,2-23 2-22,2-23 2-22,2-23 2-22,2-23	Speicheradresse Avia	MD11 MD11 MD11 MD11	IC1 IC2 IC9 TP442	66 43 43 P\$1	2-22,2-23 2-22,2-23 2-22,2-23 2-22,2-23	Daten Avia Speicher
MA8	TP427	P\$1	2-22,2-23		MD12	IC1	62	2-22,2-23	Daten Avia Speicher

Signal	Bauteil Part	Pin	Page	Beschreibung Description	Signal	Bauteil Part	Pin	Seite Page	Beschreibung Description
MD12 MD12 MD12	IC2 IC9 TP443		2-22,2-23 2-22,2-23 2-22,2-23		PST0 PST0 PST0	IC5 JP2 TP201	15	2-8,2-9 2-8,2-9 2-8,2-9	Prozessor Status
MD13 MD13 MD13 MD13	IC1 IC2 IC9 TP444	59 46 46 P\$1	2-22,2-23 2-22,2-23 2-22,2-23 2-22,2-23	Daten Avia Speicher	PST1 PST1 PST1	IC5 JP2 TP205	14	2-8,2-9 2-8,2-9 2-8,2-9	Prozessor Status
MD14 MD14 MD14 MD14	IC1 IC2 IC9 TP445	56 48 48	2-22,2-23 2-22,2-23 2-22,2-23 2-22,2-23	Daten Avia Speicher	PST2 PST2 PST2	IC5 JP2 TP200	13	2-8,2-9 2-8,2-9 2-8,2-9	Prozessor Status
MD15 MD15 MD15	IC1 IC2 IC9	53 49 49	2-22,2-23 2-22,2-23 2-22,2-23	Daten Avia Speicher	PST3 PST3 PST3	IC5 JP2 TP204	12	2-8,2-9 2-8,2-9 2-8,2-9	Prozessor Status
MD15	TP446		2-22,2-23		PST_CLK PST_CLK	JP2 R47	24 1	2-8,2-9 2-2,2-3	Prozessor Clock
O_CLKOUT O_CLKOUT O_CLKOUT	IC17 IC24 IC25	15 33 26	2-32,2-33 2-30,2-31 2-30,2-31	Transport Stream Byte Clock	R R R	CON1 L6 TP84	1 2 P\$1	2-28,2-29 2-28,2-29 2-28,2-29	Audio Ausgang rechts am Stecker
O_D/P O_D/P O_D/P	IC17 IC24 IC25	5 21 27	2-32,2-33 2-30,2-31 2-30,2-31	Output Data Tuner	R+ R+	CON3 L2	3 14	2-16,2-17 2-16,2-17	Receive Daten + Ethernet
O_D/F O_D0 O_D0 O_D0	IC17 IC24 IC25	21 25 18	2-30,2-31 2-32,2-33 2-30,2-31 2-30,2-31	Datenleitung Tuner	R- R-	CON3 L2	6 16	2-16,2-17 2-16,2-17	Receive Daten - Ethernet
O_D1 O_D1 O_D1	IC17 IC24 IC25	20 26 19	2-32,2-33 2-30,2-31 2-30,2-31	Datenleitung Tuner	R/W R/W R/W R/W	IC1 IC16 IC17 IC5	32 56 63	2-20,2-21 2-12,2-13 2-32,2-33 2-2,2-3	Read/Write Datenbus
O_D2 O_D2 O_D2	IC17 IC24 IC25	19 27 20	2-32,2-33 2-30,2-31 2-30,2-31	Datenleitung Tuner	R/W R/W RST	RN9 TP528 IC16	7 P\$1 8	2-2,2-3 2-20,2-21 2-12,2-13	Reset
O_D3 O_D3 O_D3	IC17 IC24 IC25	12 28 21	2-32,2-33 2-30,2-31 2-30,2-31	Datenleitung Tuner	RST RST RXD+	IC8 TP350 IC8	75 P\$1 91	2-16,2-17 2-12,2-13 2-16,2-17	Receive Daten +
O_D4 O_D4 O_D4	IC17 IC24 IC25	11 29 22	2-32,2-33 2-30,2-31 2-30,2-31	Datenleitung Tuner	RXD+ RXD+ RXD+	L2 R97 TP114	3 1 P\$1	2-16,2-17 2-16,2-17 2-16,2-17	Ethernet
O_D5 O_D5 O_D5	IC17 IC24 IC25	10 30 23	2-32,2-33	Datenleitung Tuner	RXD- RXD- RXD- RXD-	IC8 L2 R97 TP112	92 1 2 P\$1	2-16,2-17 2-16,2-17 2-16,2-17 2-16,2-17	Receive Daten - Ethernet
O_D6 O_D6 O_D6	IC17 IC24 IC25	7 31 24	2-32,2-33 2-30,2-31 2-30,2-31	Datenleitung Tuner	SCKE SCKE SCKE	IC27 RN7 TP531	37 8 P\$1	2-4,2-5 2-4,2-5 2-4,2-5	Synchron DRAM Clock enable
O_D7 O_D7 O_D7	IC17 IC24 IC25	6 32 25	2-32,2-33 2-30,2-31 2-30,2-31	Datenleitung Tuner	SCL SCL SCL	IC10 IC24 IC25	8 17 12	2-26,2-27 2-30,2-31 2-30,2-31	Clock I2C-Bus
O_ERROR O_ERROR O_ERROR	IC17 IC24 IC25	3 22 29	2-32,2-33 2-30,2-31 2-30,2-31	Error detected Tuner	SCL SCL SCL SCL	IC31 IC4 IC5 IC7	1	2-10,2-11 2-24,2-25 2-2,2-3 2-10,2-11	
O_STROUT O_STROUT O_STROUT	IC17 IC24 IC25	4 20 28	2-32,2-33 2-30,2-31 2-30,2-31	Synchronisation Byte Signal Tuner	SCL SCL SCL SCL	JP1 RN15 RN2 TP49	12 2 4 P\$1	2-36 2-2,2-3 2-34,2-35 2-2,2-3	
PIC_LED1 PIC_LED1 PIC_LED1	IC7 JP13 TP212	12 7 P\$1	2-10,2-11 2-10,2-11 2-10,2-11	Standby LED	SD16 SD16 SD16	IC27 RN28 TP237	42 4 P\$1	2-4,2-5 2-4,2-5 2-4,2-5	Datenleitung Speicher
POWER_ON POWER_ON POWER_ON	IC7	14 11 1	2-36 2-10,2-11 2-10,2-11	Power On ATX Netzteil	SD17 SD17 SD17	IC27 RN28 TP231	3	2-4,2-5 2-4,2-5 2-4,2-5	Datenleitung Speicher
PP0 PP0 PP0	IC5 JP1 TP197	2	2-2,2-3 2-36 2-2,2-3	Paralleler Ausgang Coldfire für Smartcard	SD18 SD18 SD18	IC27 RN28 TP216	2	2-4,2-5 2-4,2-5 2-4,2-5	Datenleitung Speicher
PP1 PP1 PP1	IC5 JP1 TP198	4	2-2,2-3 2-36 2-2,2-3	siehe PP0	SD19 SD19 SD19	IC27 RN28 TP215	47 1 P\$1	2-4,2-5 2-4,2-5 2-4,2-5	Datenleitung Speicher
PP2 PP2 PP2	IC5 JP1 TP199	6	2-2,2-3 2-36 2-2,2-3	siehe PP0	SD20 SD20 SD20	IC27 RN29 TP176	48 4 P\$1	2-4,2-5 2-4,2-5 2-4,2-5	Datenleitung Speicher

Signal	Bauteil Part	Pin	Seite Page	Beschreibung Description	Signal	Bauteil Part		Seite Page	Beschreibung Description
SD21 SD21 SD21	IC27 RN29 TP172	50 3 P\$1	2-4,2-5 2-4,2-5 2-4,2-5	Datenleitung Speicher	TCK TCK TCK TCK	IC16 IC17 JP5 RN5	59 2 8 4	2-12,2-13 2-32,2-33 2-12,2-13 2-12,2-13	Programmierung Lattice CLK Boundary Scan Xilinx
SD22 SD22 SD22	IC27 RN29 TP171	51 2 P\$1	2-4,2-5 2-4,2-5 2-4,2-5	Datenleitung Speicher	TDI TDI TDI	IC16 JP5 RN5	16 3 2	2-12,2-13 2-12,2-13 2-12,2-13	Programmierung Lattice
SD23 SD23 SD23	IC27 RN29 TP170	53 1 P\$1	2-4,2-5 2-4,2-5 2-4,2-5	Datenleitung Speicher	TDI/DSI TDI/DSI TDI/DSI	IC5 JP2 RN17	153 8 7	2-8,2-9 2-8,2-9 2-8,2-9	Development Serial Input Coldfire Debug Port
SD24 SD24 SD24	IC27 RN26 TP169	2 4 P\$1	2-4,2-5 2-4,2-5 2-4,2-5	Datenleitung Speicher	TDO TDO	IC17 JP5	34 2	2-32,2-33 2-12,2-13	Boundary Scan Xilinx
SD25 SD25 SD25	IC27 RN26 TP168	4 3 P\$1	2-4,2-5 2-4,2-5 2-4,2-5	Datenleitung Speicher	TDO/DSO TDO/DSO TDO_DVB1	IC5 JP2 IC16	151 10 87	2-8,2-9 2-8,2-9 2-12,2-13	Development Serial Output Coldfire Debug Port Steuerleitung Programm.
SD26 SD26	IC27 RN26	5 2	2-4,2-5 2-4,2-5	Datenleitung Speicher	TDO_DVB1 TDO_DVB1	IC17 TP345	32 P\$1	2-32,2-33 2-12,2-13	Xilinx
SD26 SD27 SD27	TP167 IC27 RN26	P\$1 7 1	2-4,2-5 2-4,2-5	Datenleitung Speicher	TMS TMS TMS TMS	IC16 IC17 JP5 RN5	37 142 6 3	2-12,2-13 2-32,2-33 2-12,2-13 2-12,2-13	Programmierung Lattice Boundary Scan Xilinx
SD27 SD28 SD28	TP166 IC27 RN27	P\$1 8 4	2-4,2-5 2-4,2-5	Datenleitung Speicher	TRST/DSC TRST/DSC TRST/DSC	IC5 JP2 RN17	149 4 8	2-8,2-9 2-8,2-9 2-8,2-9	Development Serial Clock
SD28 SD29 SD29	TP160 IC27 RN27	P\$1 10 3	2-4,2-5 2-4,2-5	Datenleitung Speicher	TTX TTX TTX	IC17 IC4 TP533	23 44 P\$1	2-32,2-33 2-24,2-25 2-24,2-25	Teletext Bit Stream
SD29 SD30 SD30 SD30	TP159 IC27 RN27 TP158	11 2	2-4,2-5 2-4,2-5 2-4,2-5	Datenleitung Speicher	TTXRQ TTXRQ TTXRQ	IC17 IC4 TP532	22 43 P\$1	2-32,2-33 2-24,2-25 2-24,2-25	Teletext Request
SD31 SD31 SD31	IC27 RN27 TP131	13 1	2-4,2-5 2-4,2-5 2-4,2-5 2-4,2-5	Datenleitung Speicher	TUNE_27M TUNE_27M TUNE_27M TUNE_27M	C163 IC10 R124 R43	1 3 1 1	2-26,2-27 2-26,2-27 2-26,2-27 2-26,2-27	Regelung 27MHz
SDA SDA SDA SDA SDA SDA SDA SDA SDA	IC10 IC24 IC25 IC31 IC4 IC5 IC7 JP1	9 18 13 3 42 103 2 10	2-26,2-27 2-30,2-31 2-30,2-31 2-10,2-11 2-24,2-25 2-2,2-3 2-10,2-11 2-36	Datenleitung I2C-Bus	TUNE_27M  TUN_RES TUN_RES TUN_RES  TXD+ TXD+ TXD+ TXD+	TP535 IC40 T3 TP536 C101 L2 R5	2	2-26,2-27 2-6,2-7 2-30,2-31 2-30,2-31 2-16,2-17 2-16,2-17 2-16,2-17	Tuner Reset  Transmit Data Ethernet
SDA SDA SDA	RN15 RN2 TP50	1 3 P\$1	2-2,2-3 2-34,2-35 2-2,2-3		TXD+ TXD- TXD-	TP540 C101 L2	P\$1 2 6	2-16,2-17 2-16,2-17 2-16,2-17	Transmit Data Ethernet
SD_BCLK SD_BCLK SD_BCLK	IC27 R132 TP322	38 2 P\$1	2-4,2-5 2-6,2-7 2-4,2-5	Busclock	TXD- TXD-	R6 TP539	1 P\$1	2-16,2-17 2-16,2-17	
SD_CLK SD_CLK SD_CLK SD_CLK	IC1 IC2 IC9 TP361	84 35 35 P\$1	2-22,2-23 2-22,2-23 2-22,2-23 2-22,2-23	Clock Speicher Avia	UDQM UDQM UDQM UDQM	IC1 IC2 IC9 TP364	80 36 36 P\$1	2-22,2-23 2-22,2-23 2-22,2-23 2-22,2-23	Data I/O Mask Speicher Avia
SIZ0 SIZ0 SIZ0 SIZ0	IC16 IC17 IC5 TP353	46	2-12,2-13 2-32,2-33 2-2,2-3 2-12,2-13	Transfer Size Datenbus	VCC VCC VCC VCC	C113 C137 C169 C172 C184	1 2 1 2 1	2-36 2-30,2-31 2-20,2-21 2-30,2-31 2-30,2-31	Versorgungsspannung
SIZ1 SIZ1 SIZ1 SIZ1	IC16 IC17 IC5 TP354	77 121 47 P\$1	2-12,2-13 2-32,2-33 2-2,2-3 2-12,2-13	Transfer Size Datenbus	VCC VCC VCC VCC	C65 C84 C91 CON7 CON7	2 1 2 19 20	2-20,2-21 2-30,2-31 2-8,2-9 2-36 2-36	
SYS_UP SYS_UP SYS_UP SYS_UP	IC5 IC7 JP13 R81	198 13 8 2	2-2,2-3 2-10,2-11 2-10,2-11 2-10,2-11	System aktiv	VCC VCC VCC VCC	CON7 CON7 D20 D26 IC14	4 6 1 2 7	2-36 2-36 2-30,2-31 2-36 2-8,2-9	
T+ T+	CON3 L2	1 9	2-16,2-17 2-16,2-17	Transmit Daten Ethernet +	VCC VCC VCC VCC	IC14 IC24 IC24 IC24	8 12 14 7	2-8,2-9 2-30,2-31 2-30,2-31 2-30,2-31	
T- T-	CON3 L2	2 11	2-16,2-17 2-16,2-17	Transmit Daten Ethernet -	VCC VCC VCC	IC25 IC25 IC44	5 6 3	2-30,2-31 2-30,2-31 2-20,2-21	

Beschreibung Description

Signal	Bauteil Part	Pin	Seite Page	Beschreibung Description	Signal	Bauteil Part	Pin	Seite Page
VCC	JP1	7	2-36		VDD	C6	1	2-22,2-23
VCC	JP3	12	2-14,2-15		VDD	C61	2	2-16,2-17
VCC	JP3	14	2-14,2-15		VDD	C62	2	2-20,2-21
VCC	R111	2	2-26,2-27		VDD	C66	2	2-18,2-19
VCC	R56	2	2-4,2-5		VDD	C67	2	2-20,2-21
VCC VCC	R74 R98	2 2	2-36		VDD VDD	C68	2	2-20,2-21
VCC	TP103	1	2-36 2-36		VDD	C69 C70	2	2-20,2-21 2-20,2-21
VCC	TP104	1	2-36		VDD	C71	2	2-20,2-21
					VDD	C72	2	2-20,2-21
VDATA0	IC1	142	,	Video Daten	VDD	C73	2	2-20,2-21
VDATA0	IC4	16	2-24,2-25		VDD	C74	2	2-20,2-21
VDATA0	TP362	P\$1	2-20,2-21		VDD	C75	2	2-20,2-21
VDATA1	IC1	143	2-20,2-21	Video Daten	VDD VDD	C76 C77	2 2	2-20,2-21 2-2,2-3
VDATA1	IC4	15	2-24,2-25	video Dateri	VDD	C80	2	2-6,2-7
VDATA1	TP363	P\$1			VDD	C81	2	2-6,2-7
					VDD	C82	2	2-6,2-7
VDATA2	IC1		2-20,2-21	Video Daten	VDD	C83	2	2-6,2-7
VDATA2	IC4	14	2-24,2-25		VDD	C85	2	2-18,2-19
VDATA2	TP529	P\$1	2-20,2-21		VDD VDD	C89 C9	2	2-8,2-9 2-22,2-23
VDATA3	IC1	148	2-20,2-21	Video Daten	VDD	C92	1	2-22,2-23
VDATA3	IC4	13	2-24,2-25	rides Editori	VDD	C93	2	2-4,2-5
VDATA3	TP530	P\$1	2-20,2-21		VDD	C94	2	2-4,2-5
					VDD	C95	2	2-4,2-5
VDATA4	IC1	150	2-20,2-21	Video Daten	VDD	C96	2	2-4,2-5
VDATA4	IC4	12 D¢1	2-24,2-25		VDD	C97	2	2-4,2-5
VDATA4	TP143	P\$1	2-24,2-25		VDD VDD	C98 CON7	2 1	2-4,2-5 2-36
VDATA5	IC1	152	2-20,2-21	Video Daten	VDD	CON7	11	2-36
VDATA5	IC4	11	2-24,2-25	rides Editori	VDD	CON7	2	2-36
VDATA5	TP144	P\$1	2-24,2-25		VDD	D1	1	2-20,2-21
					VDD	D26	1	2-36
VDATA6	IC1	154	2-20,2-21	Video Daten	VDD	D27	2	2-20,2-21
VDATA6 VDATA6	IC4 TP145	10 P\$1	2-24,2-25 2-24,2-25		VDD VDD	IC1 IC1		2-20,2-21 2-20,2-21
VDAIAO	17145	ГФІ	2-24,2-25		VDD	IC1		2-20,2-21
VDATA7	IC1	155	2-20,2-21	Video Daten	VDD	IC1		2-20,2-21
VDATA7	IC4	9	2-24,2-25		VDD	IC1		2-20,2-21
VDATA7	TP146	P\$1	2-24,2-25		VDD	IC1		2-20,2-21
\/DD	010	•	0.00.000		VDD	IC1		2-20,2-21
VDD VDD	C10 C102	2 2	2-22,2-23 2-12,2-13	Versorgungsspannung	VDD VDD	IC1 IC1	17 181	2-20,2-21 2-20,2-21
VDD	C102	2	2-12,2-13		VDD	IC1		2-20,2-21
VDD	C110	2	2-18,2-19		VDD	IC1	27	2-20,2-21
VDD	C111	2	2-18,2-19		VDD	IC1	36	2-20,2-21
VDD	C112	1	2-36		VDD	IC1	47	2-20,2-21
VDD	C117	2	2-32,2-33		VDD	IC1	5	2-20,2-21
VDD VDD	C12 C121	2 1	2-22,2-23 2-2,2-3		VDD VDD	IC1 IC1	55 61	2-20,2-21 2-20,2-21
VDD	C122	1	2-4,2-5		VDD	IC1	69	2-20,2-21
VDD	C124	2	2-6,2-7		VDD	IC1	75	2-20,2-21
VDD	C126	1	2-16,2-17		VDD	IC1	81	2-20,2-21
VDD	C128	2	2-14,2-15		VDD	IC1	87	2-20,2-21
VDD	C13	2	2-22,2-23		VDD	IC1	95	2-20,2-21
VDD VDD	C133 C14	2 2	2-18,2-19 2-22,2-23		VDD VDD	IC13 IC13	7 8	2-8,2-9 2-8,2-9
VDD	C15	2	2-22,2-23		VDD	IC15	20	2-32,2-33
VDD	C16	2	2-22,2-23		VDD	IC16	12	2-12,2-13
VDD	C167	1	2-2,2-3		VDD	IC16	36	2-12,2-13
VDD	C17	2	2-22,2-23		VDD	IC16	63	2-12,2-13
VDD	C171	2	2-32,2-33		VDD	IC16	89	2-12,2-13
VDD VDD	C173 C174	2 2	2-32,2-33 2-32,2-33		VDD VDD	IC17 IC17	1	2-32,2-33 2-32,2-33
VDD	C175	2	2-32,2-33		VDD	IC17		2-32,2-33
VDD	C177	2	2-20,2-21		VDD	IC17		2-32,2-33
VDD	C18	1	2-24,2-25		VDD	IC17		2-32,2-33
VDD	C181	1	2-20,2-21		VDD	IC17	16	2-32,2-33
VDD	C20	2	2-22,2-23		VDD	IC17	35	2-32,2-33
VDD	C21	2	2-2,2-3		VDD	IC17	36	2-32,2-33
VDD VDD	C22 C23	2 2	2-2,2-3 2-2,2-3		VDD VDD	IC17 IC17	53 70	2-32,2-33 2-32,2-33
VDD	C24	2	2-2,2-3		VDD	IC17	71	2-32,2-33
VDD	C25	2	2-2,2-3		VDD	IC17	90	2-32,2-33
VDD	C26	2	2-2,2-3		VDD	IC18	16	2-14,2-15
VDD	C27	2	2-2,2-3		VDD	IC2	1	2-22,2-23
VDD	C28	2	2-2,2-3		VDD	IC2	13	2-22,2-23
VDD	C29	2	2-2,2-3		VDD	IC2	25	2-22,2-23
VDD VDD	C30 C31	2 2	2-24,2-25 2-24,2-25		VDD VDD	IC2 IC2	38 44	2-22,2-23 2-22,2-23
VDD	C32	2	2-24,2-25		VDD	IC2	7	2-22,2-23
VDD	C38	2	2-12,2-13		VDD	IC20	18	2-6,2-7
VDD	C54	2	2-2,2-3		VDD	IC20	31	2-6,2-7
VDD	C55	2	2-16,2-17		VDD	IC20	42	2-6,2-7
VDD	C57	2	2-16,2-17		VDD	IC20	7 10	2-6,2-7
VDD	C58	2	2-18,2-19		VDD	IC22	18	2-18,2-19

Signal	Bauteil Part	Pin	Seite Page	Beschreibung Description	Signal	Bauteil Part	Pin	Seite Page	Beschreibung Description
VDD	IC22	31	2-18,2-19		VDD	R2	1	2-16,2-17	
VDD	IC22	42	2-18,2-19		VDD	R24	2	2-18,2-19	
VDD	IC22	7	2-18,2-19		VDD	R36	2	2-32,2-33	
VDD VDD	IC24 IC25	24 10	2-30,2-31 2-30,2-31		VDD VDD	R37 R38	1 2	2-16,2-17 2-20,2-21	
VDD	IC25	14	2-30,2-31		VDD	R4	2	2-20,2-21	
VDD	IC27	1	2-4,2-5		VDD	R41	1	2-16,2-17	
VDD	IC27	14	2-4,2-5		VDD	R42	1	2-16,2-17	
VDD	IC27	27	2-4,2-5		VDD	R44	1	2-2,2-3	
VDD	IC27	3	2-4,2-5		VDD	R45	1	2-2,2-3	
VDD	IC27 IC27	43	2-4,2-5		VDD VDD	R46	1	2-20,2-21	
VDD VDD	IC27 IC27	49 9	2-4,2-5 2-4,2-5		VDD	R52 R53	1	2-16,2-17 2-20,2-21	
VDD	IC28	16	2-14,2-15		VDD	R54	1	2-20,2-21	
VDD	IC3	2	2-20,2-21		VDD	R59	1	2-20,2-21	
VDD	IC4	17	2-24,2-25		VDD	R60	1	2-20,2-21	
VDD	IC4	39	2-24,2-25		VDD	R62	2	2-20,2-21	
VDD	IC4	6	2-24,2-25		VDD	R64	2	2-20,2-21	
VDD VDD	IC40 IC41	20 18	2-6,2-7 2-18,2-19		VDD VDD	R65 R70	2 2	2-30,2-31 2-20,2-21	
VDD	IC41	31	2-18,2-19		VDD	R72	2	2-32,2-33	
VDD	IC41	42	2-18,2-19		VDD	R75	1	2-22,2-23	
VDD	IC41	7	2-18,2-19		VDD	R77	1	2-10,2-11	
VDD	IC5	1	2-2,2-3		VDD	R78	2	2-12,2-13	
VDD	IC5		2-2,2-3		VDD	R95	1	2-30,2-31	
VDD VDD	IC5 IC5		2-2,2-3 2-2,2-3		VDD VDD	RN1 RN1	5 6	2-18,2-19 2-18,2-19	
VDD	IC5		2-2,2-3		VDD	RN1	7	2-16,2-19	
VDD	IC5	13	2-2,2-3		VDD	RN1	8	2-18,2-19	
VDD	IC5		2-2,2-3		VDD	RN11	1	2-2,2-3	
VDD	IC5		2-2,2-3		VDD	RN11	2	2-2,2-3	
VDD VDD	IC5 IC5		2-2,2-3 2-2,2-3		VDD VDD	RN11 RN11	3 4	2-2,2-3 2-2,2-3	
VDD	IC5		2-2,2-3		VDD	RN12	1	2-2,2-3	
VDD	IC5		2-2,2-3		VDD	RN12	2	2-2,2-3	
VDD	IC5		2-2,2-3		VDD	RN12	3	2-2,2-3	
VDD VDD	IC5 IC5		2-2,2-3 2-2,2-3		VDD VDD	RN12 RN13	4 1	2-2,2-3 2-6,2-7	
VDD	IC5		2-2,2-3		VDD	RN13	3	2-6,2-7	
VDD	IC5		2-2,2-3		VDD	RN14	1	2-6,2-7	
VDD	IC5	21	2-2,2-3		VDD	RN14	3	2-6,2-7	
VDD VDD	IC5 IC5	29 37	2-2,2-3 2-2,2-3		VDD VDD	RN15 RN15	5 6	2-2,2-3 2-2,2-3	
VDD	IC5	45	2-2,2-3		VDD	RN15	7	2-2,2-3	
VDD	IC5	52	2-2,2-3		VDD	RN15	8	2-2,2-3	
VDD	IC5	57	2-2,2-3		VDD	RN16	5	2-2,2-3	
VDD	IC5	65	2-2,2-3		VDD	RN16	6	2-2,2-3	
VDD VDD	IC5 IC5	7 73	2-2,2-3 2-2,2-3		VDD VDD	RN17 RN17	1 2	2-8,2-9 2-8,2-9	
VDD	IC5	81	2-2,2-3		VDD	RN17	3	2-8,2-9	
VDD	IC5	89	2-2,2-3		VDD	RN17	4	2-2,2-3	
VDD	IC5	97	2-2,2-3		VDD	RN2	2	2-34,2-35	
VDD VDD	IC8 IC8	22 56	2-16,2-17 2-16,2-17		VDD VDD	RN30 RN30	1 4	2-20,2-21 2-20,2-21	
VDD	IC8	69	2-16,2-17		VDD	RN5	5	2-12,2-13	
VDD	IC8	9	2-16,2-17		VDD	RN5	6	2-12,2-13	
VDD	IC9	1	2-22,2-23		VDD	RN5	7	2-12,2-13	
VDD VDD	IC9 IC9	13	2-22,2-23		VDD VDD	RN5 RN6	8	2-12,2-13	
VDD	IC9	25 38	2-22,2-23 2-22,2-23		VDD	RN6	5 6	2-2,2-3 2-2,2-3	
VDD	IC9	44	2-22,2-23		VDD	RN6	7	2-2,2-3	
VDD	IC9	7	2-22,2-23		VDD	RN6	8	2-2,2-3	
VDD	JP1	5	2-36		VDD	RN9	1	2-2,2-3	
VDD VDD	JP2 JP3	25 10	2-8,2-9 2-14,2-15		VDD VDD	RN9 RN9	2 3	2-2,2-3 2-2,2-3	
VDD	JP5	1	2-14,2-13		VDD	RN9	4	2-2,2-3	
VDD	L1	2	2-16,2-17						
VDD	L25	1	2-6,2-7		VDD25	C129	2	2-20,2-21	Versorgungsspannung
VDD VDD	L5 L8	1 2	2-20,2-21		VDD25 VDD25	C130 C156	1	2-32,2-33 2-32,2-33	Avia
VDD	L9	1	2-24,2-25 2-2,2-3		VDD25 VDD25	C156	2 2	2-32,2-33 2-20,2-21	
VDD	Q6	4	2-2,2-3		VDD25	C170	2	2-32,2-33	
VDD	R1	1	2-16,2-17		VDD25	C182	2	2-20,2-21	
VDD	R10	2	2-8,2-9		VDD25	C183	2	2-20,2-21	
VDD VDD	R100 R107	1 1	2-22,2-23 2-32,2-33		VDD25 VDD25	C7 C8	1 2	2-20,2-21 2-20,2-21	
VDD	R107	2	2-32,2-33		VDD25 VDD25	D1	2	2-20,2-21	
VDD	R109	2	2-32,2-33		VDD25	D27	1	2-20,2-21	
VDD	R118	1	2-16,2-17		VDD25	IC1		2-20,2-21	
VDD VDD	R119 R122	1 2	2-16,2-17 2-26,2-27		VDD25 VDD25	IC1 IC1	12 144	2-20,2-21 2-20,2-21	
VDD	R13	2	2-32,2-33		VDD25 VDD25	IC1		2-20,2-21	
VDD	R137	2	2-4,2-5		VDD25	IC1	197	2-20,2-21	
VDD	R14	1	2-4,2-5		VDD25 VDD25	IC1	40	2-20,2-21	
VDD VDD	R140 R16	2 2	2-30,2-31 2-20,2-21		VDD25 VDD25	IC1 IC1	65 91	2-20,2-21 2-20,2-21	
VDD	R17	1	2-20,2-21		VDD25	IC17		2-32,2-33	

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Signal	Bauteil Part	Pin	Seite Page	Beschreibung Description
VDD25 VDD25 VDD25 VDD25 VDD25 VDD25 VDD25 VDD25 VDD25 VDD25 VDD25 VDD25 VDD25	IC17 IC17 IC17 IC17 IC17 IC17 IC17 IC3 IC44 R103	14 24 55 82 9 92 97 3 2	2-32,2-33 2-32,2-33 2-32,2-33 2-32,2-33 2-32,2-33 2-32,2-33 2-32,2-33 2-20,2-21 2-20,2-21 2-20,2-21	·
V_SWITCH V_SWITCH V_SWITCH V_SWITCH V_SWITCH V_SWITCH V_SWITCH V_SWITCH V_SWITCH	C1 C155 C186 C227 C254 D5 IC11 R131 R80	2 1 2 1 1 2 IN 1	2-30,2-31 2-34,2-35 2-34,2-35 2-34,2-35 2-34,2-35 2-34,2-35 2-30,2-31 2-34,2-35 2-34,2-35	Vorspannung Tuner 14/18V
V_VCC	C120 C19 C41 C42 C43 C44 IC4 IC4 IC4 IC4 IC4 IC4 IC43 L8 TP534	1 2 2 2 2 2 2 25 28 31 36 8 1 P\$1	2-24,2-25 2-28,2-29 2-24,2-25 2-24,2-25 2-24,2-25 2-24,2-25 2-24,2-25 2-24,2-25 2-24,2-25 2-24,2-25 2-28,2-29 2-24,2-25 2-24,2-25 2-24,2-25	Versorgungsspannung Video
WRST WRST	IC14 TP279	6 P\$1	2-8,2-9 2-8,2-9	Reset
XTUN XTUN XTUN	IC40 R135 TP538	9 1 P\$1	2-6,2-7 2-26,2-27 2-6,2-7	Regelung 27MHz
X_BCLK X_BCLK X_BCLK	IC17 R133 TP537	88 2 P\$1	2-32,2-33 2-6,2-7 2-32,2-33	Busclock
Y Y Y	L17 L3 R82 TP163	1 2 2 P\$1	2-28,2-29 2-24,2-25 2-24,2-25 2-28,2-29	Farbsignal SVideo
ZERO ZERO ZERO ZERO ZERO	D21 IC10 R110 T9 TP266	1 16 1 2 P\$1	2-26,2-27 2-26,2-27 2-26,2-27 2-26,2-27 2-26,2-27	Tonabschaltung

Ersatzteilliste / Spare Parts List SeleXX PDR 5000 S DIG

#### GRUNDIG

## Ersatzteilliste Spare Parts List

### SAT

# 10 / 2000

#### **PDR 5000 S DIG**

MATERIAL-NR. / PART NO.: 774009715100 BESTELL-NR. / ORDER NO.: GAG3142

			DEGILLE WILL OUDER NO. GAGGIAZ
POS. NR. ABB. POS. NO. FIG.	MATERIAL-NR. ANZ. PART NUMBER QTY.	BEZEICHNUNG D	DESCRIPTION GB
	774009715100	PDR 5000 S DIG KEIN E-TEIL	PDR 5000 S DIG NO SPARE PART
0001.000 0002.000 0003.000 0004.000 0005.000 0006.000 0007.000	759880570100 759880570200 759880570700 720117131600 759880570800 759880570900 759880571000 759880570300 759880570400 759880570500 759880570600 720116002001	GEHBODEN GEHOBERTEIL KABEL STVB 2XBU 90GD IDE FERNBEDIENUNG TP 777 FRONTPLATTE KPL. SILBER FUSS SCHWARZ NETZKABEL LP-MEDIA-TV LP-BEDIENTEIL LP-NETZTEILMODUL ATX FESTPLATTE BEDIENUNGSANLEITUNG D/GB	BOTTOM COVER CABLE STVB 2XBU 90GD IDE REMOTE CONTROL TP 777 FRONTPANEEL CPL. SILVER FOOT BLACK POWER CABLE MEDIA-TV BOARD CONTROLBOARD POWER SUPPLY BOARD ATX HARDDISC INSTRUCTION MANUAL D/GB

Es gelten die Vorschriften und Sicherheitshinweise gemäß dem Service Manual "Sicherheit", Mat.-Nummer 720108000000, sowie zusätzlich die eventuell abweichenden, landesspezifischen Vorschriften!



The regulations and safety instructions shall be valid as provided by the "Safety" Service Manual, part number 720108000000, as well as the respective national deviations.

ÄNDERUNGEN VORBEHALTEN / SUBJECT TO ALTERATION